500mA Variable/Fixed Output LDO Regulators

BDxxKA5 Series BDxxKA5W Series BD00KA5W Series

●General Description

The BDxxKA5 series are low-saturation regulators that are available for output currents up to 500mA. The output voltage precision is ±1%. These LDO regulators are offered in several output voltages and package lineups with or without ON/OFF switches (that set the circuit current to 0μA at shutdown). This series can be used for a broad spectrum of applications ranging from TVs and car audio systems to HDDs, PCs, and DVDs. There regulators have a built-in overcurrent protection circuit that prevents the destruction of the IC, due to output short circuits and a thermal shutdown circuit.

●Features

- Output voltage precision: ±1%
- Low-saturation voltage with PMOS output: 0.12V Typ.(Io=200mA)
- Built-in over-current protection circuit
- Built-in thermal shutdown circuit
- Shutdown switch (BDxxKA5WFP and BDxxKA5WF series)
- Ceramic capacitor compatible (recommended capacitance: 1μF or greater)

●Key Specifications

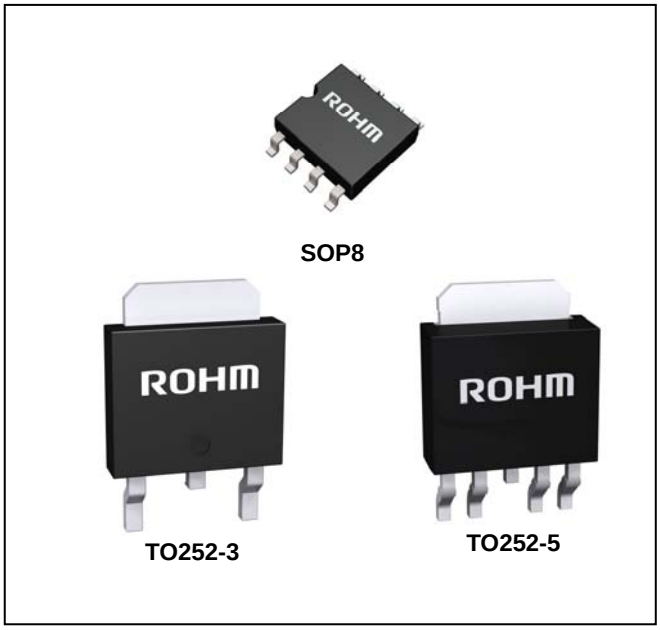
- Input Power Supply Voltage: 5.5V (Max.)
- Output voltage type: BA00KA5 Variable
  BAxxKA5 Fixed
- Output current: 500mA (Max.)
- Operating temperature range: -40℃ to +105℃

●Applications

Microcontrollers and all electronic devices that use logic circuit

●Packages

	W (Typ.) x D (Typ.) x H (Max.)
SOP8	5.00mm x 6.20mm x 1.71mm
TO252-3	6.50mm x 9.50mm x 2.50mm
TO252-5	6.50mm x 9.50mm x 2.50mm



●Lineup matrix

Part Number	Output Voltage (V)								Package
	1.0	1.2	1.5	1.8	2.5	3.0	3.3	Variable	
BDxxKA5WFP	○	○	○	○	○	○	○	○	TO252-5
BDxxKA5WF	○	○	○	○	○	○	○	○	SOP8
BDxxKA5FP	○	○	○	○	○	○	○	–	TO252-3

●Ordering Information

B D x x K A 5 W x x - E 2

Part Number	Output voltage 00:Variable Other: Fixed	Current capacity KA5: 500mA	Shutdown Switch W: Include None:without	Package FP : TO252-3 TO252-5 F : SOP8	Packaging and forming specification E2: Embossed tape and reel

○Product structure : Silicon monolithic integrated circuit ○This product is not designed protection against radioactive rays.

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●**Lineup**

Maximum output current (Max.)	Shutdown Switch	Package		Output voltage (Typ.)	Orderable Part Number
500mA	With Switch	TO252-5	Reel of 2000	1.0V	BD10KA5WFP-E2
				1.2V	BD12KA5WFP-E2
				1.5V	BD15KA5WFP-E2
				1.8V	BD18KA5WFP-E2
				2.5V	BD25KA5WFP-E2
				3.0V	BD30KA5WFP-E2
				3.3V	BD33KA5WFP-E2
				Variable	BD00KA5WFP-E2
		SOP8	Reel of 2500	1.0V	BD10KA5WF-E2
				1.2V	BD12KA5WF-E2
				1.5V	BD15KA5WF-E2
				1.8V	BD18KA5WF-E2
				2.5V	BD25KA5WF-E2
				3.0V	BD30KA5WF-E2
				3.3V	BD33KA5WF-E2
				Variable	BD00KA5WF-E2
	No Switch	TO252-3	Reel of 2000	1.0V	BD10KA5FP-E2
				1.2V	BD12KA5FP-E2
				1.5V	BD15KA5FP-E2
				1.8V	BD18KA5FP-E2
				2.5V	BD25KA5FP-E2
				3.0V	BD30KA5FP-E2
				3.3V	BD33KA5FP-E2

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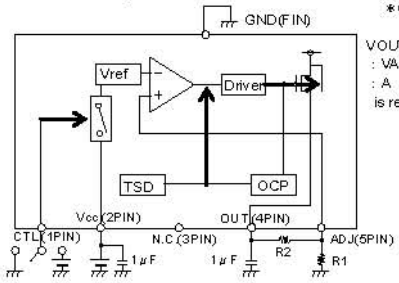
●Block Diagrams / Standard Example Application Circuits / Pin Configurations / Pin Descriptions

[BD00KA5WFP]

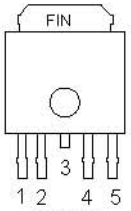


＊Output voltage configuration

VOUT=VADJ× (R1+R2) ÷R1(V)
: VADJ=0.75V(Typ.)
: A value of approximately 30kΩ is recommended for R1.

Fig.1

TOP VIEW



TO252-5

TO252-5(BD00KA5WFP)

Pin No.	PinName	Function
1	CTL	Output voltage ON/OFF control
2	Vcc	Power supply voltage input
3	N.C.	Unconnected terminal
4	OUT	Voltage output
5	ADJ	Output voltage configuration terminal
FIN	GND	GND

[BDxxKA5WFP]

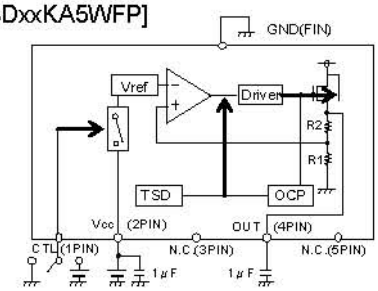
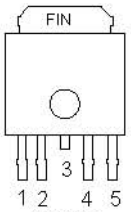


Fig.2

TOP VIEW



TO252-5

TO252-5(BDxxKA5WFP)

Pin No.	Pin Name	Function
1	CTL	Output voltage ON/OFF control
2	Vcc	Power supply voltage input
3	N.C.	Unconnected terminal
4	OUT	Voltage output
5	N.C.	Unconnected terminal
FIN	GND	GND

[BDxxKA5FP]

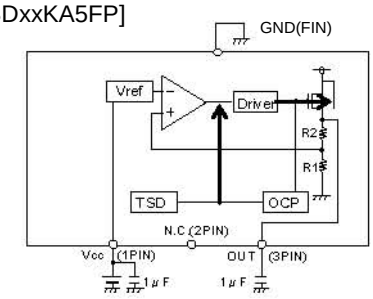
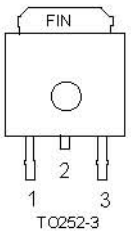


Fig.3

TOP VIEW

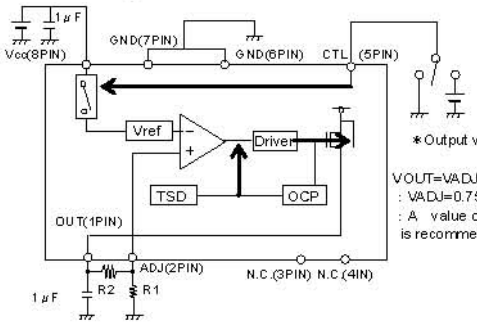


TO252-3

TO252-5(BDxxKA5FP)

Pin No.	Pin Name	Function
1	Vcc	Power supply voltage input
2	N.C.	Unconnected terminal
3	OUT	Voltage output
FIN	GND	GND

[BD00KA5WF]

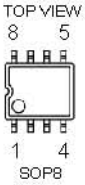


＊Output voltage configuration

VOUT=VADJ× (R1+R2) ÷R1(V)
: VADJ=0.75V(Typ.)
: A value of approximately 30kΩ is recommended for R1.

Fig.4

TOP VIEW



SOP8

SOP8(BD00KA5WF)

Pin No.	Pin Name	Function
1	OUT	Voltage output
2	ADJ	Output voltage configuration terminal
3	N.C.	Unconnected terminal
4		
5	CTL	Output voltage ON/OFF control
6	GND	GND
7		
8	Vcc	Power supply voltage input

[BDxxKA5WF]

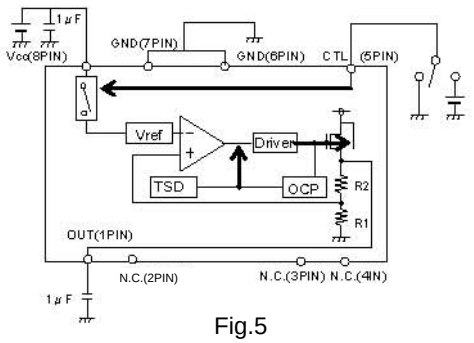
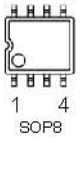


Fig.5

TOP VIEW



SOP8

SOP8(BDxxKA5WF)

Pin No.	Pin Name	Function
1	OUT	Voltage output
2	N.C.	Unconnected terminal
3		
4		
5	CTL	Output voltage ON/OFF control
6	GND	GND
7		
8	Vcc	Power supply voltage input

N.C. pins are electrically open to the inside of the IC chip.

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●Absolute Maximum Ratings (Ta=25℃)

Parameter		Symbol	Limits	Unit.
Power Supply Voltage		Vcc	-0.3 to +7.0[*1]	V
Output Control Terminal Voltage		VCTL	-0.3 to Vcc[*1]	V
Power Dissipation	TO252-3	Pd	1200[*2]	mW
	TO252-5		1300[*3]	
	SOP8		687.6[*4]	
Operating Temperature Range		Topr	-40 to +105	℃
Ambient Storage Temperature		Tstg	-55 to +150	℃
Maximum Junction Temperature		Tjmax	150	℃

*1 Must not exceed Pd
*2 When a 70mm×70mm×1.6mm glass epoxy board is used. Reduce by 9.6 mW/℃ over 25℃.
*3 When a 70mm×70mm×1.6mm glass epoxy board is used. Reduce by 10.4mW/℃ over 25℃.
*4 When a 70mm×70mm×1.6mm glass epoxy board is used. Reduce by 5.5 mW/℃ over 25℃.

●Recommended Operating Ratings (Ta=25℃)

Parameter	Symbol	Min.	Max.	Unit.
Input Power Supply Voltage	Vcc	2.3	5.5	V
Output Current	Io	0	500	mA
Output Voltage Configuration Range[*5]	Vo	1.0	4.0	V
Output Control Terminal Voltage	VCTL	0	Vcc	V

*5 Only BD00KA5WFP and BD00KA5WF

●Electrical Characteristics

BDxxKA5 Series BDxxKA5W Series
(Unless otherwise specified,Ta=25℃, VCTL =2V, Vcc=2.5V(Vo=1.0V,1.2V,1.5V,1.8V), Vcc=3.3V(Vo=2.5V), Vcc=5.0V(Vo=3.0V,3.3V))

Parameter	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Output Voltage	Vo	Vo(T)-0.015	Vo(T)	Vo(T)+0.015	V	Io=200mA (Vo=1.0V,1.2V)
		Vo(T)×0.99	Vo(T)	Vo(T)×1.01		Io=200mA (Vo≧1.5V)
Shut Down Current	Isd	−	0	1	μA	VCTL=0V, Io=0mA (OFFmode)
Bias Current	Ib	−	350	550	μA	Io=0mA
Dropout Voltage [*6]	ΔVd	−	0.12	0.20	V	Io=200mA, Vcc=0.95×Vo
Peak Output Current	Io	500	−	−	mA	
Ripple Rejection	R.R.	−	50	−	dB	f=120Hz, ein[※9]=-10dBV, Io=100mA
Line Regulation [*7]	Reg.I	−	10	35	mV	Vcc=Vo+0.5V→5.5V, Io=200mA
Load Regulation	Reg.L	−	25	75	mV	Io=0mA→500mA
Temperature Coefficient of Output Voltage [*8]	Tcvo	−	±100	−	ppm/℃	Io=5mA,Tj=0 to 125℃
CTL ON Mode Voltage	VCTLON	2.0	−	−	V	ACTIVE MODE, Io=0mA
CTL OFF Mode Voltage	VCTLOFF	−	−	0.8	V	OFF MODE, Io=0mA
CTL Input Current	ICTL	20	40	60	μA	Io=0mA

Vo(T) : Output Voltage
*6 Vo≧2.5V
*7 1.0≦Vo≦1.8V,Vcc=2.3V→5.5V
*8 Not 100% tested
*9 ein : Input Voltage Ripple

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●**Electrical Characteristics – continued**

BD00KA5W Series

(Unless otherwise specified, Ta=25℃, Vcc=2.5V, V_{CTL}=2V ,Vo=1.5V)

Parameter	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Shut Down Current	Isd	−	0	1	μA	VCTL=0V, Io=0mA (OFFmode)
Bias Current	Ib	−	350	550	μA	Io=0mA
Reference Voltage	VADJ	0.742	0.750	0.758	V	Io=50mA
Dropout Voltage *10	ΔVd	−	0.12	0.20	V	Io=200mA, Vcc=0.95×Vo
Peak Output Current	Io	500	−	−	mA	
Ripple Rejection	R.R.	−	50	−	dB	f=120Hz, ein※12=-10dBV, Io=100mA
Line Regulation	Reg.I	−	10	35	mV	Vcc=Vo+0.5V→5.5V, Io=200mA
Load Regulation	Reg.L	−	25	75	mV	Io=0mA→500mA
Temperature Coefficient of Output Voltage *11	Tcvo	−	±100	−	ppm/℃	Io=5mA,Tj=0 to 125℃
CTL ON Mode Voltage	VCTLON	2.0	−	−	V	ACTIVE MODE, Io=0mA
CTL OFF Mode Voltage	VCTLOFF	−	−	0.8	V	OFF MODE, Io=0mA
CTL Input Current	ICTL	20	40	60	μA	Io=0mA

*10 Vo≧2.5V

*11 Not 100% tested

*12 ein : Input Voltage Ripple

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●**Typical Performance Curves**

(Unless specified otherwise, Vcc=25V,V_{CTL} =2V,and Io=0mA)

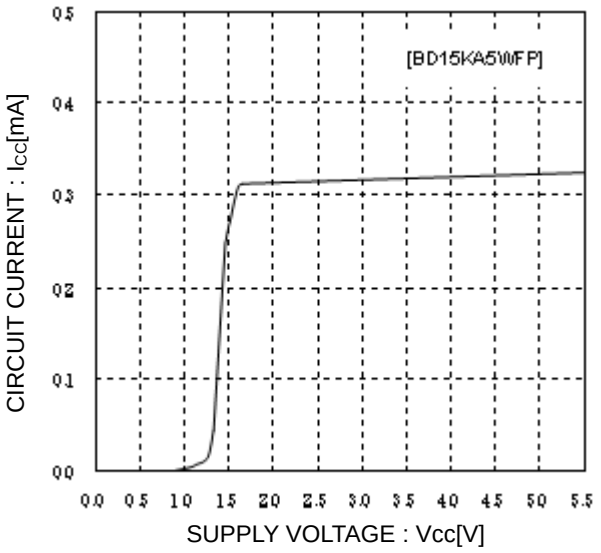

Fig.6
Circuit current

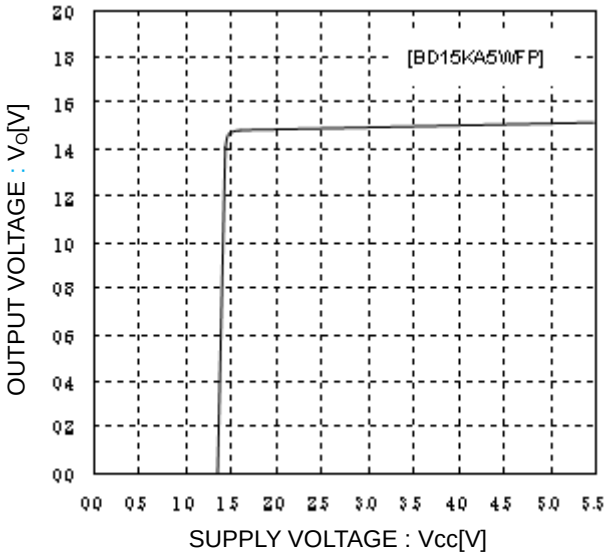

Fig.7
Input Stability（Io=0mA）

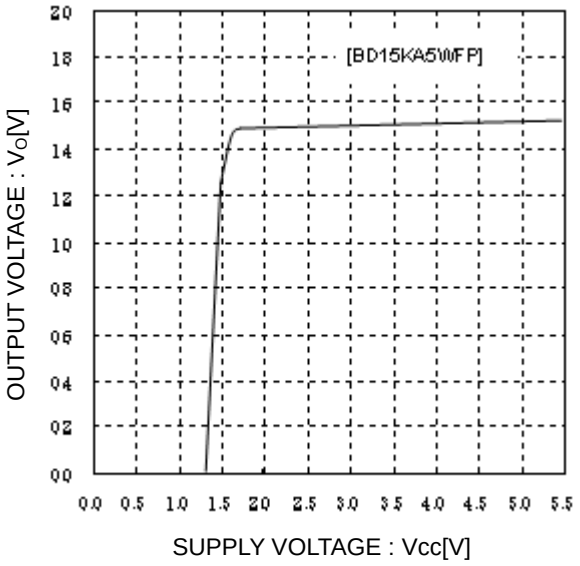

Fig.8
Input Stability
（Io=500mA）

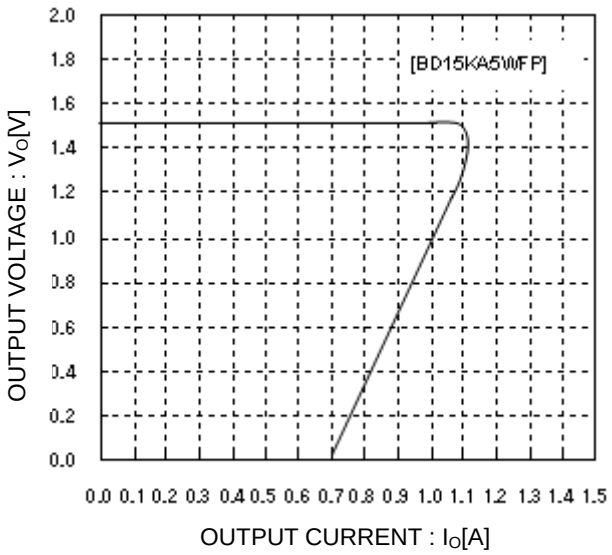

Fig.9
Load Stability

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●**Typical Performance Curves - continued**

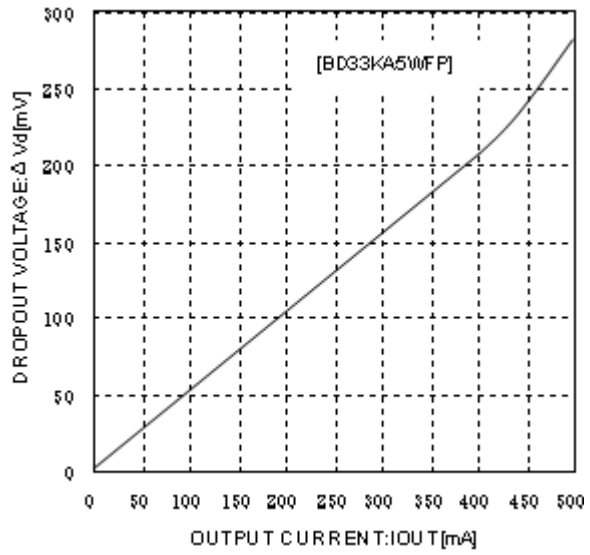

Fig.10
Input/Output Voltage Difference
（Vcc=3.135V）

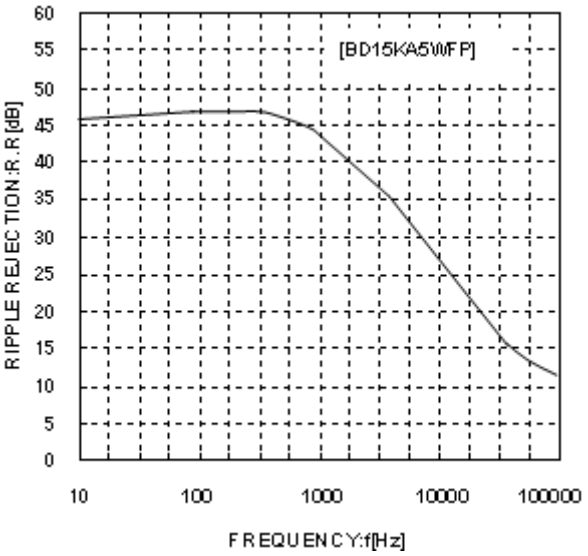

Fig.11
Ripple Rejection
（ein=10dBV,Io=100mA）

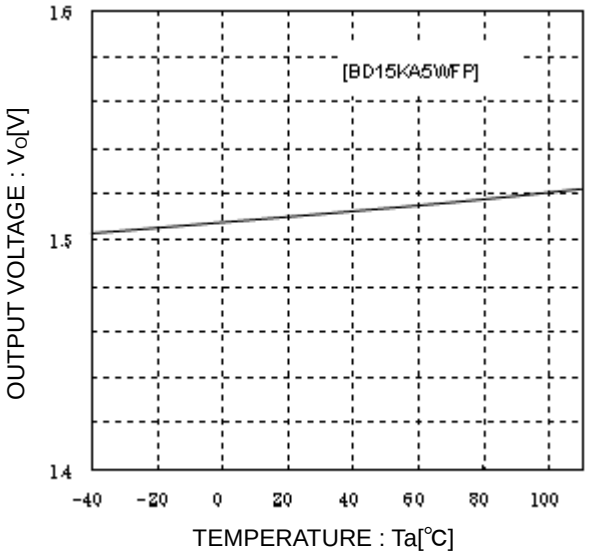

Fig.12
Output Voltage
（Io=5mA）

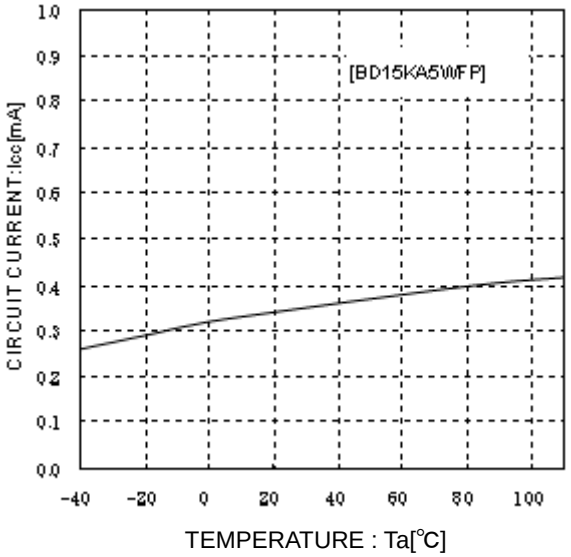

Fig.13
Circuit Current
Temperature Characteristics

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●**Typical Performance Curves - continued**

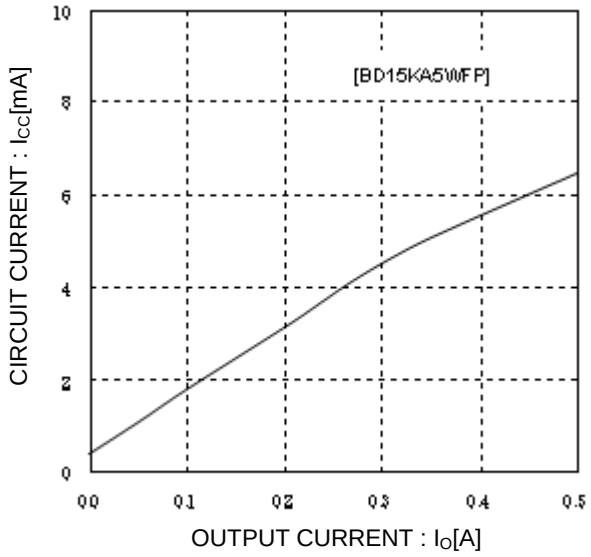

Fig.14
Circuit Current by load Level

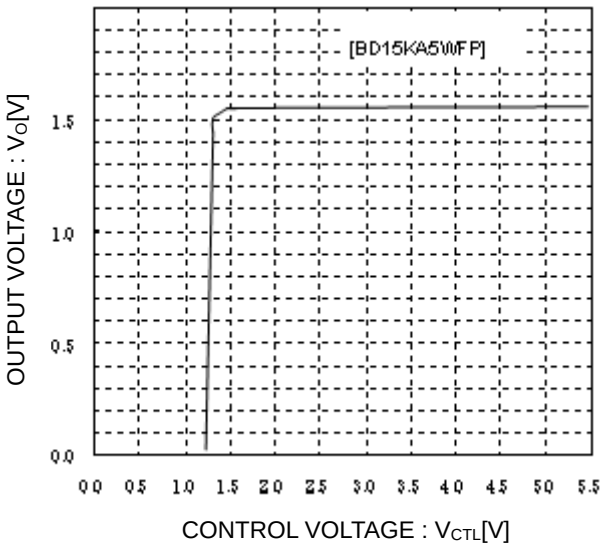

Fig.15
CTL Voltage vs. Output Voltage

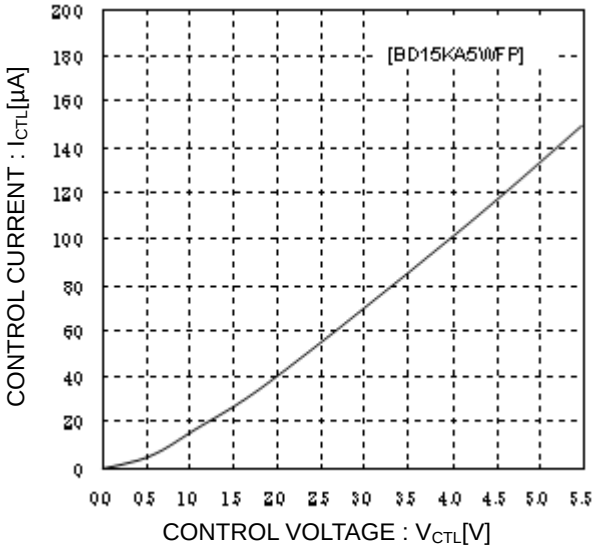

Fig.16
CTL Voltage vs. Output Current

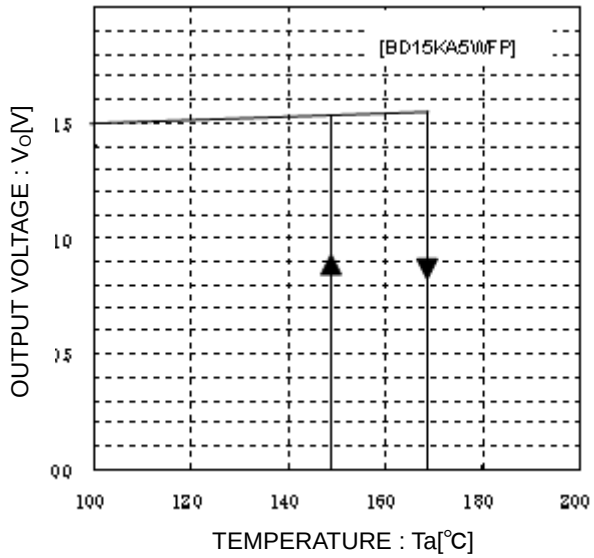

Fig.17
Thermal Shutdown
Circuit Characteristics (Io=5mA)

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●**I/O equivalence circuit**

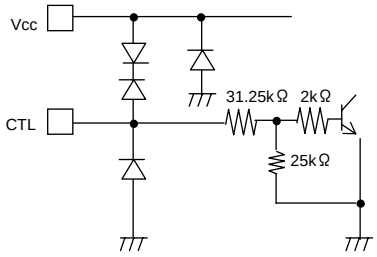

Fig.18

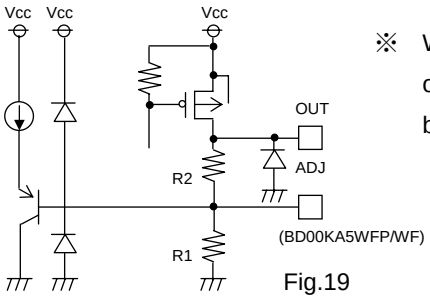

Fig.19

※ With BD00KA5WFP/WF,R1and R2 are connected outside the IC between ADJ and GND and between OUT and ADJ.

●**Power Dissipation**

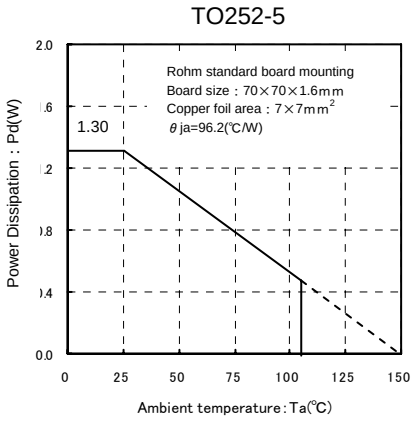

Fig.20 Power Dissipation heat reducing characteristics

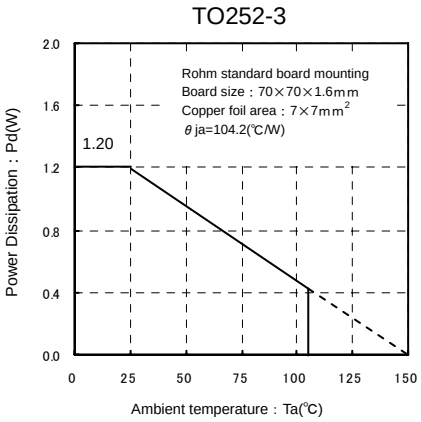

Fig.21 Power Dissipation heat reducing characteristics

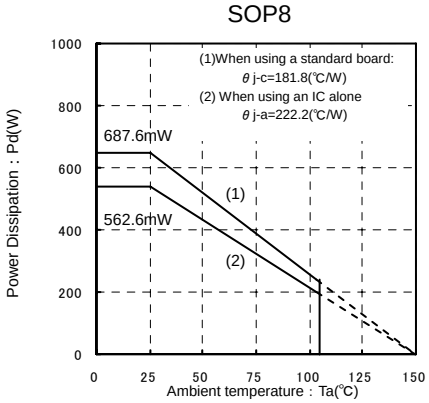

Fig.22 Power Dissipation heat reducing characteristics

When using at temperatures over Ta=25℃, please refer to the power dissipation shown in Fig.20 through 22.
The IC characteristics are closely related to the temperature at which the IC is used, so if the temperature exceeds the maximum junction temperature T_{jMAX}, the device may malfunction or be destroyed. The heat of the IC requires sufficient consideration regarding instantaneous destruction and long-term operation reliability. In order to protect the IC from thermal damage, it is necessary to operate it at temperatures less than the maximum junction temperature T_{jMAX}.
Even when the ambient temperature Ta is a normal temperature (25℃), the chip(junction) temperature Tj may be quite high, so please operate the IC at temperatures less than the acceptable loss Pd.

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The calculation method for power consumption Pc(W) is as follows :

$Pc = (Vcc-Vo)\times Io+Vcc\times Icca$
Acceptable loss $Pd \geqq Pc$

Vcc : Input voltage
Vo : Output voltage
Io : Load current
Icca : Circuit current

Solving for the load current I_O in order to operate within the acceptable loss,

$$Io \leqq \frac{Pc - Vcc\times Icca}{Vcc - Vo}$$

It is then possible to find the maximum load current I_{OMAX} with respect to the applied voltage Vcc at the time of thermal design.

Calculation Example

Example 1) When Ta=85℃, Vcc=2.5V, Vo=1.0V

$$Io \leqq \frac{0.676 - 2.5\times Icca}{2.5\text{-}1.0}$$
$Io \leqq 440mA$ (Icca : 6mA)

BA10KA5WFP（TO252-5 packaging）
θja=96.2℃/W → -10.4mW/℃
25℃=1300mW → 85℃=676mW

Please refer to the above information and keep thermal designs within the scope of acceptable loss for all operating temperature ranges.
The power consumption Pc of the IC when there is a short circuit (short between Vo and GND) is :

$Pc=Vcc\times(Icca+Ishort)$
*Ishort : Short circuit current

●Terminal Vicinity Settings and Cautions

・Vcc Terminal
Please attach a capacitor (greater than 1μF) between Vcc and GND.
The capacitance values differ depending on the application, so chose a capacitor with sufficient margin and verify the operation on actual board.

・GND Terminal
Please be sure to keep the set ground and IC ground at the same potential level so that a potential difference does not arise between them. If a potential difference arises between the set ground and the IC ground, the preset voltage will not be output properly, causing the system to become unstable. Please reduce the impedance by making the ground patterns as wide as possible and reducing the distance between the set ground and the IC ground as much as possible.

・CTL Terminal

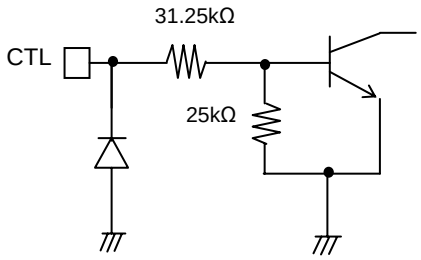

Fig.23 Input equivalent circuit

The CTL terminal is turned ON at 2.0V and higher, and OFF at 0.8V and lower, within the operating power supply voltage range. The power supply and the CTL terminal may be started up and shut down in any order without problems.

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●Vo Terminal

Please be sure to attach an anti-oscillation capacitor between V_O and GND.

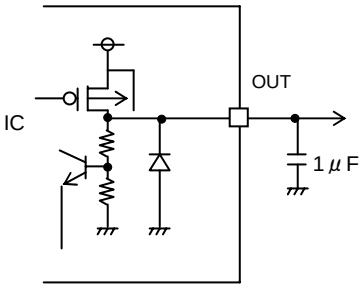


Fig.24 Output Equivalent Circuit

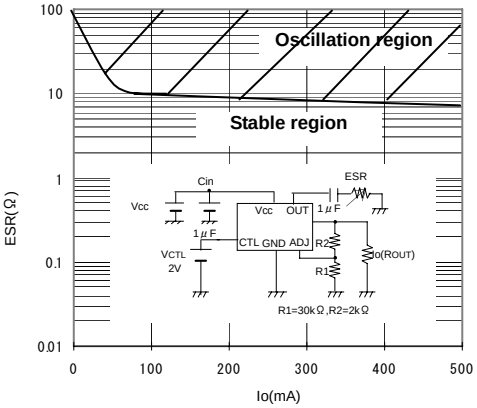
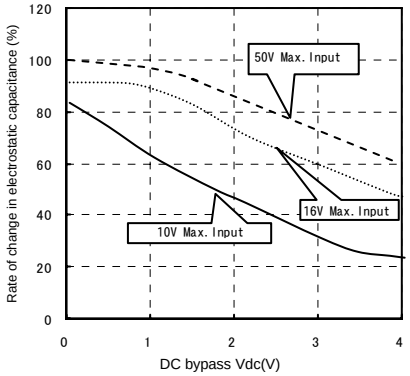
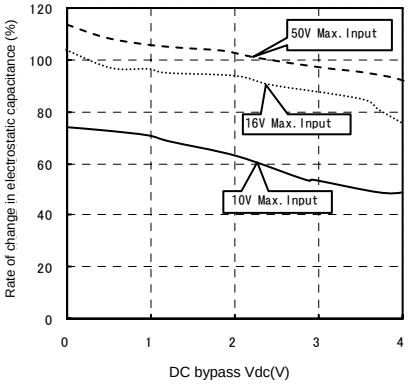


Fig.25 ESR-Io Characteristics

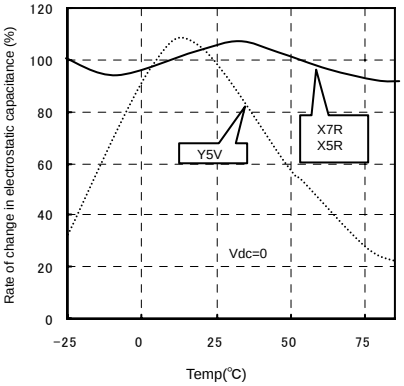
Be sure to place an anti-oscillation capacitor between the output terminal and the GND. Oscillations may arise if the capacitance value changes, due to factors such as temperature changes. A 1μF capacitor with small internal series resistance (ESR) such as a ceramic capacitor is recommended as an anti-oscillation capacitor. Ceramic capacitors generally have favorable temperature characteristics and DC bypass characteristics. When selecting a ceramic capacitor, a high voltage capacitor (good DC bypass characteristics) with temperature characteristics that are superior to those of X5R or X7R, is recommended. In applications where input voltage and load fluctuations are rapid, please decide on a capacitor after sufficiently confirming its properties according to its specifications in the actual application.



(a) Capacitance-bypass characteristics (Y5V)



(b) Capacitance-bypass characteristics (X5R,X7R)



(C) Capacitance-temperature characteristics (X5R,X7R,Y5V)

Fig.26 General characteristics of ceramic capacitors

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●Operational Notes

○Protection Circuits

Over-current Protection Circuit

A built-in over-current protection circuit corresponding to the current capacity prevents the destruction of the IC when there are load shorts. This protection circuit is a “7”-shaped current control circuit that is designed such that the current is restricted and does not latch even when a large current momentarily flows through the system with a high-capacitance capacitor. However, while this protection circuit is effective for the prevention of destruction due to unexpected accidents, it is not suitable for continuous operation or transient use. Please be aware when creating thermal designs that the over-current protection circuit has negative current capacity characteristics with regard to temperature.

○Thermal Shutdown Circuit (Thermal Protection)

This system has a built-in temperature protection circuit for the purpose of protecting the IC from thermal damage. As shown in Fig. 20-22, this must be used within the range of acceptable loss, but if the acceptable loss is continuously exceeded, the chip temperature Tj increases, causing the thermal shutdown circuit to operate. When the thermal shutdown circuit operates, the operation of the circuit is suspended. The circuit resumes operation immediately after the chip temperature Tj decreases, so the output repeats the ON and OFF states (Please refer to Fig.17 for the temperatures at which the temperature protection circuit operates).

There are cases in which the IC is destroyed due to thermal runaway when it is left in the overloaded state. Be sure to avoid leaving the IC in the overloaded state.

○Reverse Current

In order to prevent the destruction of the IC when a reverse current flows through the IC, it is recommended that a diode be placed between the Vcc and Vo and a pathway be created so that the current can escape (Refer to Fig.27).

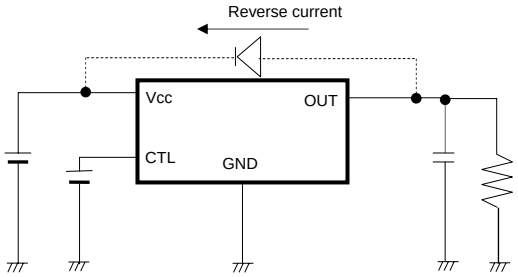

Fig.27 Bypass diode

○This IC is BI-CMOS IC that has a P-board (substrate) and P+ isolation between each element, as shown in Fig.28. A P-N junction is formed between this P-layer and the N-layer of each element, and the P-N junction operates as :

- a parasitic diode when the electric potential relationship is GND> Pin A, GND> Pin B, or
- a parasitic transistor when the electric potential relationship is Pin B > GND> Pin A.

Parasitic elements are structurally inevitable in the IC. The operation of parasitic elements induces mutual interference between circuits, causing malfunctions and eventually the destruction of the IC. Take precaution as not to use the IC in ways that would cause parasitic elements to operate. For example, applying a voltage that is lower than the GND (P-board) to the input terminal.

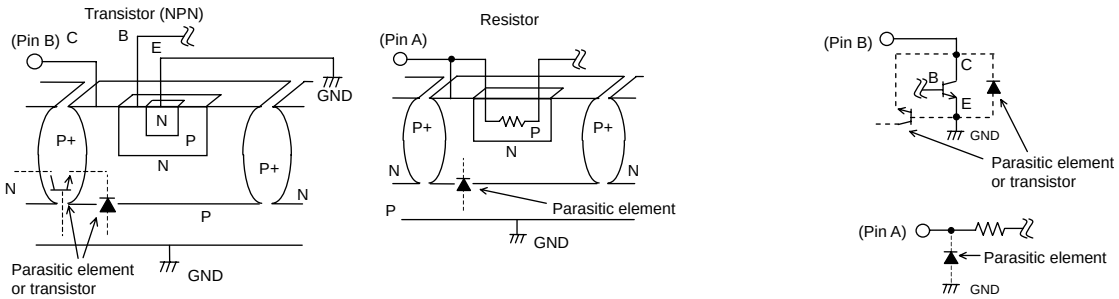

Fig.28 Basic structure example

Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority.

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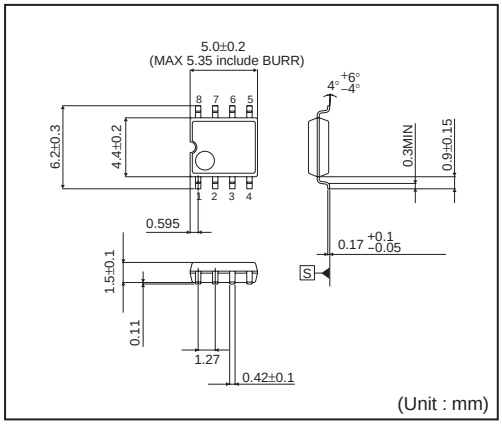
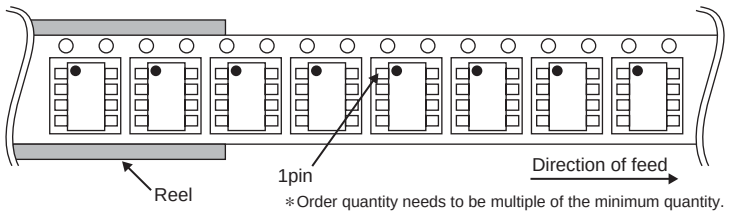
●Physical Dimension Tape and Reel Information

SOP8



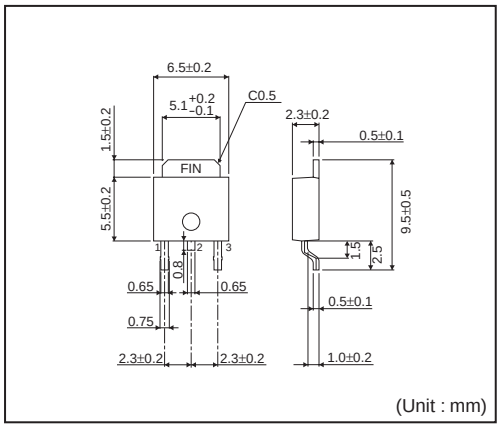
<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	E2 (The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



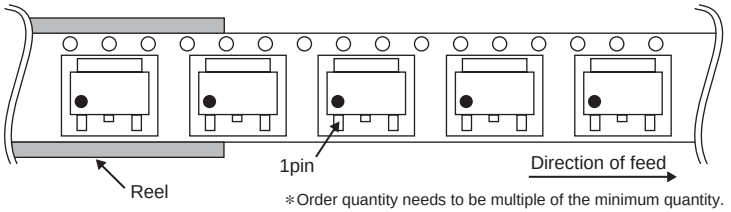
∗Order quantity needs to be multiple of the minimum quantity.

TO252-3



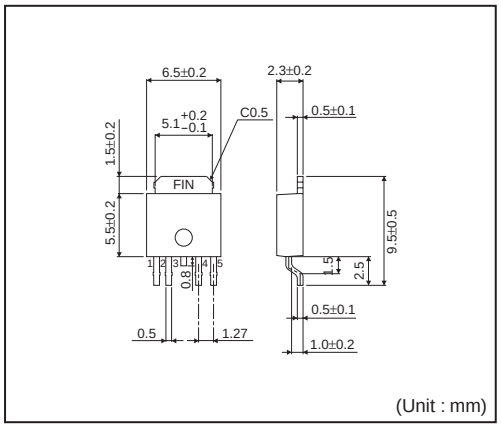
<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2000pcs
Direction of feed	E2 (The direction is the 1pin of product is at the lower left when you hold reel on the left hand and you pull out the tape on the right hand)



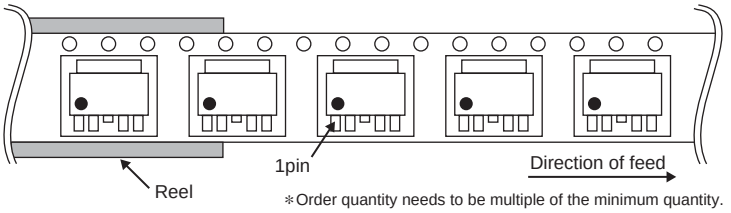
∗Order quantity needs to be multiple of the minimum quantity.

TO252-5



<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2000pcs
Direction of feed	E2 (The direction is the 1pin of product is at the lower left when you hold reel on the left hand and you pull out the tape on the right hand)



∗Order quantity needs to be multiple of the minimum quantity.

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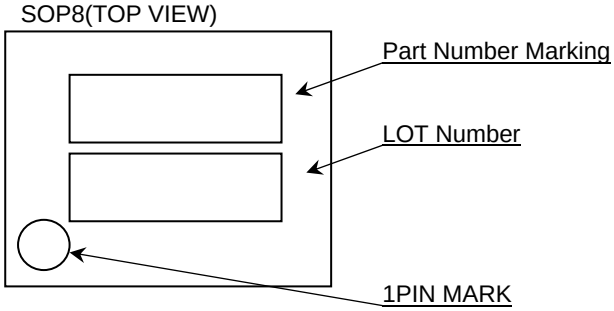
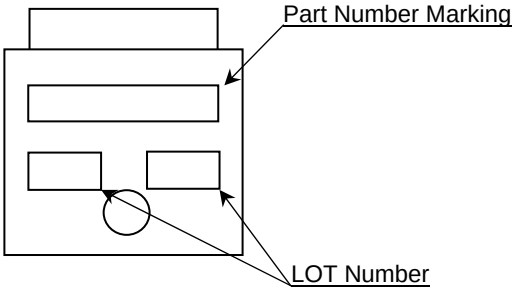
●**Marking Diagrams**

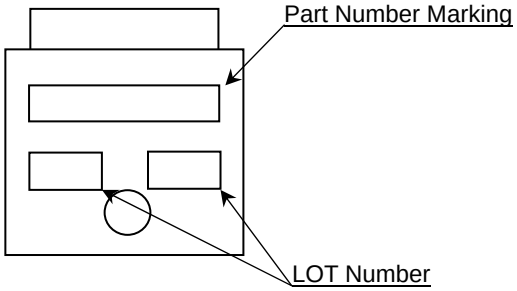
SOP8(TOP VIEW)



TO252-3
(TOP VIEW)



TO252-5
(TOP VIEW)



Orderable Part Number	Package	Part Number Marking
BD10KA5WFP-E2	TO252-5	10KA5W
BD12KA5WFP-E2		12KA5W
BD15KA5WFP-E2		15KA5W
BD18KA5WFP-E2		18KA5W
BD25KA5WFP-E2		25KA5W
BD30KA5WFP-E2		30KA5W
BD33KA5WFP-E2		33KA5W
BD00KA5WFP-E2		00KA5W
BD10KA5WF-E2	SOP8	10KA5W
BD12KA5WF-E2		12KA5W
BD15KA5WF-E2		15KA5W
BD18KA5WF-E2		18KA5W
BD25KA5WF-E2		25KA5W
BD30KA5WF-E2		30KA5W
BD33KA5WF-E2		33KA5W
BD00KA5WF-E2		00KA5W
BD10KA5FP-E2	TO252-3	10KA5
BD12KA5FP-E2		12KA5
BD15KA5FP-E2		15KA5
BD18KA5FP-E2		18KA5
BD25KA5FP-E2		25KA5
BD30KA5FP-E2		30KA5
BD33KA5FP-E2		33KA5

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●**Revision History**

Date	Revision	Changes
26.Jun.2012	001	New Release

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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4) The Products are not subject to radiation-proof design.

5) Please verify and confirm characteristics of the final or mounted products in using the Products.

6) In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse) is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7) De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.

8) Confirm that operation temperature is within the specified range described in the product specification.

9) ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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1) When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2) In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

●Precautions Regarding Application Examples and External Circuits

1) If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2) You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

●Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

●Precaution for Storage / Transportation

1) Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2) Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3) Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4) Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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●Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

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