



Ordering Information

С	0805	Υ	104	K	5	R	Α	С	TU
Ceramic	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish ¹	Packaging/ Grade (C-Spec)
	0603 0805 1206 1210 1812	Y = Floating Electrode with Flexible Termination	Two signifcant digits + number of zeros	J = ±5% K = ±10% M = ±20%	9 = 6.3 8 = 10 4 = 16 3 = 25 5 = 50 1 = 100 2 = 200 A = 250	R = X7R	A = N/A	C = 100% Matte Sn L = SnPb (5% Pb minimum)	See "Packaging C-Spec Ordering Options Table" below

 $^{^{7}}$ Additional termination fnish options may be available. Contact KEMET for details.

 $^{^{1}}$ SnPb termination f nish option is not available on automotive grade product.



Packaging C-Spec Ordering Options Table

Packaging Type	Packaging/Grade Ordering Code (C-Spec)						
Commercial Grade ¹							
Bulk Bag	Not Required (Blank)						
7" Reel/Unmarked	TU						
13" Reel/Unmarked	7411 (EIA 0603 and smaller case sizes) 7210 (EIA 0805 and larger case sizes)						
7" Reel/Marked	TM						
13" Reel/Marked	7040 (EIA 0603 and smaller case sizes) 7215 (EIA 0805 and larger case sizes)						
7" Reel/Unmarked/2 mm pitch ²	7081						
13" Reel/Unmarked/2 mm pitch ²	7082						
Automoti	ve Grade ³						
7" Reel	AUTO						
13" Reel/Unmarked	AUTO7411 (EIA 0603 and smaller case sizes) AUTO7210 (EIA 0805 and larger case sizes)						
7" Reel/Unmarked/2 mm pitch ²	3190						
13" Reel/Unmarked/2 mm pitch ²	3191						

¹ Default packaging is "Bulk Bag". An ordering code C-Spec is not required for "Bulk Bag" packaging.

Bene fts

- -55°C to +125°C operating temperature range
- Superior fex performance (up to 5 mm)
- Floating Electrode/fail open design
- Low to mid capacitance fex mitigation
- Lead (Pb)-free, RoHS and REACH compliant
- EIA 0603, 0805, 1206, 1210, and 1812 case sizes
- DC voltage ratings of 6.3 V, 10 V, 16 V, 25 V, 50 V, 100 V, 200 V, and 250 V

- Capacitance offerings ranging from 180 pF to 0.22 μF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- Commercial & Automotive (AEC-Q200) grades available
- Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination fnish allowing for excellent solderability
- SnPb termination fnish option available upon request (5% Pb minimum)

Applications

Typical applications include circuits with a direct battery or power source connection, critical and safety relevant circuits without (integrated) current limitation and any application that is subject to high levels of board fexure or temperature cycling. Examples include raw power input side filtering (power plane/bus), high current applications (automobile battery line) and circuits that cannot be fused to open. Markets include consumer, medical, industrial (power supply), automotive, aerospace and telecom.

¹ The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked.

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

³ Reeling tape options (Paper or Plastic) are dependent on capacitor case size (L" x W") and thickness dimension. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information".

³ For additional Information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information".

³ All Automotive packaging C-Specs listed exclude the option to laser mark components. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking".

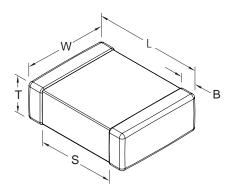


Automotive C-Spec Information

KEMET Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council.



Dimensions – Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separationkr	ess



Electrical Parameters/Characteristics

Item	Parameters/Characteristics				
Operating Temperature Range	-55°C to +125°C				
Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC)	±15%				
¹ Aging Rate (Maximum % Capacitance Loss/Decade Hour)	3.0%				
² Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5±1 seconds and charge/discharge not exceeding 50mA)				
³ Dissipation Factor (DF) Maximum Limit at 25°C	5%(6.3V & 10V), 3.5%(16V & 25V) and 2.5%(50V to 250V)				
⁴ Insulation Resistance (IR) Minimum Limit at 25°C	See Insulation Resistance Limit Table (Rated voltage applied for 120±5 seconds at 25°C)				

¹ Regarding Aging Rate: Capacitance measurements (including tolerance) are indexed to a referee time of 1,000 hours.

1kHz ± 50Hz and 1.0 ± 0.2 Vrms if capacitance ≤10µF

120Hz \pm 10Hz and 0.5 ± 0.1 Vrms if capacitance $>10\mu$ F

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON".

Post Environmental Limits

	High Temperature Life, Biased Humidity, Moisture Resistance										
Dielectric	Rated DC Voltage	Capacitance Value	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance						
	> 25		3.0		100. (1.11.1						
X7R	16/25	All	5.0	±20%	10% of Initial Limit						
	< 16		7.5		Lilling						

Insulation Resistance Limit Table (X7R Dielectric)

EIA Case Size	1,000 Megohm Microfarads or 100 GΩ	500 Megohm Microfarads or 10 GΩ
0201	N/A	ALL
0402	< 0.012 μF	≥ 0.012 µF
0603	< 0.047 µF	≥ 0.047 µF
0805	< 0.15 μF	≥ 0.15 µF
1206	< 0.47 μF	≥ 0.47 µF
1210	< 0.39 μF	≥ 0.39 µF
1808	ALL	N/A
1812	< 2.2 μF	≥ 2.2 µF
1825	ALL	N/A
2220	< 10 μF	≥ 10 µF
2225	ALL	N/A

² DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

 $^{^{\}rm 3}$ Capacitance and dissipation factor (DF) measured under the following conditions:

⁴ To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to $G\Omega$ limit. Select the lower of the two limits.



Table 1A – Capacitance Range/Selection Waterfall (0603 – 0805 Case Sizes)66



Table 1B - Capacitance Range/Selection Waterfall (1206 - 1812 Case Sizes)

			se Si Serie	ize/				C12	06Y							C12	10Y					C.	1812	<u>Υ</u>	
Capacitance	Cap Code	Vol	tage C	ode	9	8	4	3	5	1	2	Α	9	8	4	3	5	1	2	Α	3	5	1	2	Α
	Code	Katt	ed Volt (VDC)	laye																					



Table 2A – Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity ¹	Plastic Quantity			
Code	Size ¹	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel		
CJ	0603	0.80 ± 0.15	4,000	15,000	0	0		
DR	0805	0.78 ± 0.20	0	0	4,000	10,000		
DD	0805	0.90 ± 0.10	0	0	4,000	10,000		
DG	0805	1.25 ± 0.15	0	0	2,500	10,000		
EQ	1206	0.78 ± 0.20	4,000	10,000	4,000	10,000		
ER	1206	0.90 ± 0.20	0	0	4,000	10,000		
FN	1210	0.78 ± 0.20	0	0	4,000	10,000		
FQ	1210	0.90 ± 0.20	0	0	4,000	10,000		
FX	1210	0.95 ± 0.20	0	0	4,000	10,000		
GB	1812	1.00 ± 0.10	0	0	1,000	4,000		
Thickness	Case	Thickness ±	7" Reel	13" Reel	7" Reel	13" Reel		
Code	Size ¹	Range (mm)	Paper Q	uantity ¹	Plastic Quantity			

Package quantity based on fnished chip thickness specifications.

Table 2B - Bulk Packaging Quantities

Dooko	ring Tuno	Loose P	Loose Packaging						
Packa	ging Type	Bulk Bag (default)							
Packagi	ing C-Spec ¹	N,	/A ²						
Cas	e Size	Packaging Quantities ((pieces/unit packaging)						
EIA (in)	Metric (mm)	Minimum	Maximum						
0402	1005								
0603	1608								
0805	2012		50,000						
1206	3216								
1210	3225	1							
1808	4520								
1812	4532								
1825	4564		20,000						
2220	5650								
2225	5664								

¹ The "Packaging C-Spec" is a 4 to 8 digit code which identifes the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See "Ordering Information" section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products.

¹ If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

² A packaging C-Spec (see note 1 above) is not required for "Bulk Bag" packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging.



Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC-7351

EIA Size Code	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)					ı	Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)				
Couc	Couc	С	Υ	Х	V1	V2	С	Υ	Х	V1	V2	С	Υ	Х	V1	V2	
0603	1608	0.85	1.25	1.10	4.00	2.10	0.75	1.05	1.00	3.10	1.50	0.65	0.85	0.90	2.40	1.20	
0805	2012	0.99	1.44	1.66	4.47	2.71	0.89	1.24	1.56	3.57	2.11	0.79	1.04	1.46	2.42	1.81	
1206	3216	1.59	1.62	2.06	5.85	3.06	1.49	1.42	1.96	4.95	2.46	1.39	1.22	1.86	4.25	2.16	
1210	3225	1.59	1.62	3.01	5.90	4.01	1.49	1.42	2.91	4.95	3.41	1.39	1.22	2.81	4.25	3.11	
1812	4532	2.14.36	18 (2)-2.														



Soldering Process

Recommended Soldering Technique:

- Solder wave or solder refow for EIA case sizes 0603, 0805 and 1206
- All other EIA case sizes are limited to solder refow only

Recommended Refow Soldering Profle:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase refow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profle conditions for convection and IR refow refect the profle conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three refow passes at these conditions.

Profle Feature	Terminati	ion Finish		
TTOTICTCATAIC	SnPb	100% Matte Sn		
Preheat/Soak				
Temperature Minimum (T _{Smin})	100°C	150°C		
Temperature Maximum (T _{Smax})	150°C	200°C		
Time (t_s) from T_{Smin} to T_{Smax}	60 - 120 seconds	60 - 120 seconds		
Ramp-Up Rate (T _L to T _P)	3°C/second maximum	3°C/second maximum		
Liquidous Temperature (T_L)	183°C	217°C		
Time Above Liquidous (t _L)	60 - 150 seconds	60 - 150 seconds		
Peak Temperature (T _P)	235°C	260°C		
Time Within 5°C of Maximum Peak Temperature (t _p)	20 seconds maximum	30 seconds maximum		
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly refow.

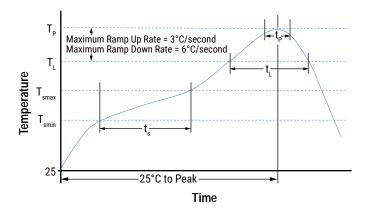




Table 4 – Performance & Reliability: Test Methods and Conditions

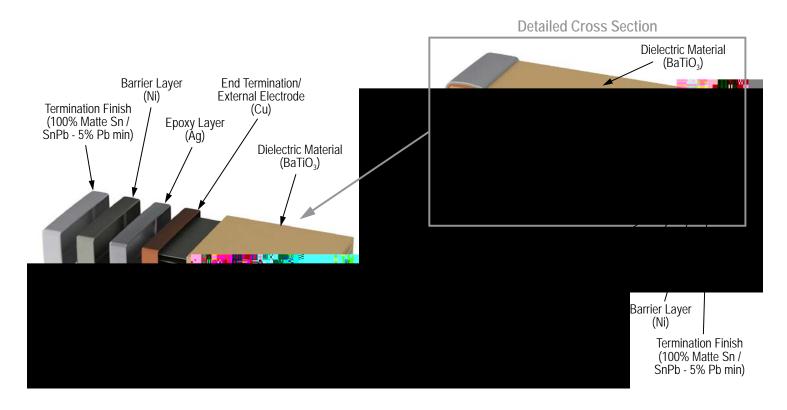
Stress	Reference	Test or Inspection Method
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for COG. Flexible termination system – 3.0 mm (minimum).
		Magnif cation 50 X. Conditions:
Caldarability	J-STD-002	a) Method B, 4 hours at 155°C, dry heat at 235°C
Solderability	J-21D-002	b) Method B at 215°C category 3
		c) Method D, category 3 at 260°C
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 4 hours after test conclusion.
B. 111 .15.	MIL-STD-202 Method	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion.
Biased Humidity	103	Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion.
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Measurement at 24 hours +/- 4 hours after test conclusion.
Thermal Shock	MIL-STD-202 Method 107	–55°C/+125°C. Note: Number of cycles required – 300, maximum transfer time – 20 seconds, dwell time – 15 minutes. Air – Air.
High Temperature Life	MIL-STD-202 Method 108 /EIA-198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature-reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fuctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction





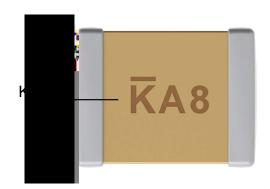
Capacitor Marking (Optional):

These surface mount multilayer ceramic capacitors are normally supplied unmarked. If required, they can be marked as an extra cost option. Marking is available on most KEMET devices but must be requested using the correct ordering code identifer(s). If this option is requested, two sides of the ceramic body will be laser marked with a "K" to identify KEMET, followed by two characters (per EIA-198 - see table below) to identify the capacitance value. EIA 0603 case size devices are limited to the "K" character only.

Laser marking option is <u>not</u> available on:

- · COG, Ultra Stable X8R and Y5V dielectric devices.
- EIA 0402 case size devices.
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive Grade stacked devices.
- · X7R dielectric products in capacitance values outlined below.

Marking appears in legible contrast. Illustrated below is an example of an MLCC with laser marking of "KA8", which designates a KEMET device with rated capacitance of 100 µF. Orientation of marking is vendor optional.





Capacitor Marking (Optional) cont'd

	C	anacita	ance (pl	F) For \	/arious	Alpha/	Numera	al Identi:	fers	
0.1.1	Capacitance (pF) For Various Alpha/Numeral Identifers Numeral									
Alpha	9	0	1	2	3	4	5	6	7	8
Character	Capacitance (pF)									
Α	0.10	1.0	10	100	1,000	10,000	100,000	1,000,000	10,000,000	100,000,000
В	0.11	1.1	11	110	1,100	11,000	110,000	1,100,000	11,000,000	110,000,000
С	0.12	1.2	12	120	1,200	12,000	120,000	1,200,000	12,000,000	120,000,000
D	0.13	1.3	13	130	1,300	13,000	130,000	1,300,000	13,000,000	130,000,000
Е	0.15	1.5	15	150	1,500	15,000	150,000	1,500,000	15,000,000	150,000,000
F	0.16	1.6	16	160	1,600	16,000	160,000	1,600,000	16,000,000	160,000,000
G	0.18	1.8	18	180	1,800	18,000	180,000	1,800,000	18,000,000	180,000,000
Н	0.20	2.0	20	200	2,000	20,000	200,000	2,000,000	20,000,000	200,000,000
J	0.22	2.2	22	220	2,200	22,000	220,000	2,200,000	22,000,000	220,000,000
K	0.24	2.4	24	240	2,400	24,000	240,000	2,400,000	24,000,000	240,000,000
L	0.27	2.7	27	270	2,700	27,000	270,000	2,700,000	27,000,000	270,000,000
М	0.30	3.0	30	300	3,000	30,000	300,000	3,000,000	30,000,000	300,000,000
N	0.33	3.3	33	330	3,300	33,000	330,000	3,300,000	33,000,000	330,000,000
Р	0.36	3.6	36	360	3,600	36,000	360,000	3,600,000	36,000,000	360,000,000
Q	0.39	3.9	39	390	3,900	39,000	390,000	3,900,000	39,000,000	390,000,000
R	0.43	4.3	43	430	4,300	43,000	430,000	4,300,000	43,000,000	430,000,000
S	0.47	4.7	47	470	4,700	47,000	470,000	4,700,000	47,000,000	470,000,000
T	0.51	5.1	51	510	5,100	51,000	510,000	5,100,000	51,000,000	510,000,000
U	0.56	5.6	56	560	5,600	56,000	560,000	5,600,000	56,000,000	560,000,000
V	0.62	6.2	62	620	6,200	62,000	620,000	6,200,000	62,000,000	620,000,000
W	0.68	6.8	68	680	6,800	68,000	680,000	6,800,000	68,000,000	680,000,000
Х	0.75	7.5	75	750	7,500	75,000	750,000	7,500,000	75,000,000	750,000,000
Υ	0.82	8.2	82	820	8,200	82,000	820,000	8,200,000	82,000,000	820,000,000
Z	0.91	9.1	91	910	9,100	91,000	910,000	9,100,000	91,000,000	910,000,000
а	0.25	2.5	25	250	2,500	25,000	250,000	2,500,000	25,000,000	250,000,000
b	0.35	3.5	35	350	3,500	35,000	350,000	3,500,000	35,000,000	350,000,000
d	0.40	4.0	40	400	4,000	40,000	400,000	4,000,000	40,000,000	400,000,000
е	0.45	4.5	45	450	4,500	45,000	450,000	4,500,000	45,000,000	450,000,000
f	0.50	5.0	50	500	5,000	50,000	500,000	5,000,000	50,000,000	500,000,000
m	0.60	6.0	60	600	6,000	60,000	600,000	6,000,000	60,000,000	600,000,000
n	0.70	7.0	70	700	7,000	70,000	700,000	7,000,000	70,000,000	700,000,000
t	0.80	8.0	80	800	8,000	80,000	800,000	8,000,000	80,000,000	800,000,000
у	0.90	9.0	90	900	9,000	90,000	900,000	9,000,000	90,000,000	900,000,000



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

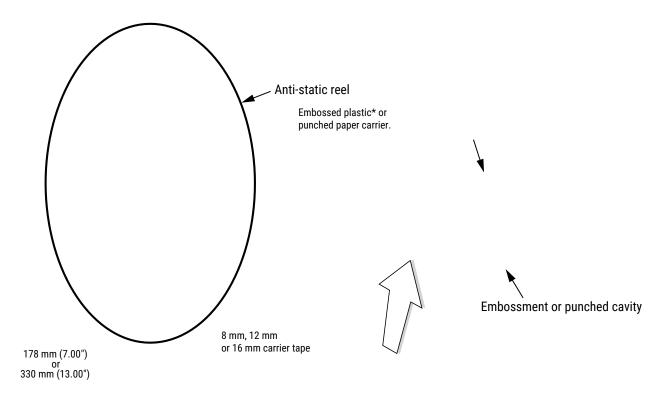




Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

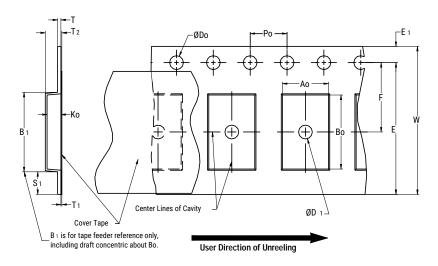




Figure 2 – Punched (Paper) Carrier Tape Dimensions

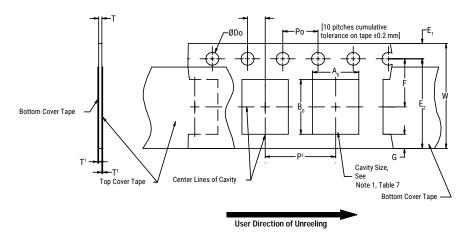


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)							
Tape Size	D ₀	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2
8 mm							



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength				
8 mm	0.1 to 1.0 Newton (10 to 100 gf)				
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)				

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA Standards 556 and 624.

Figure 3 – Maximum Component Rotation

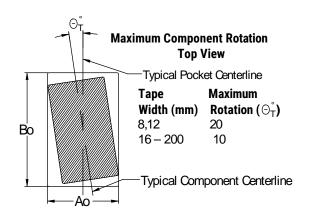


Figure 4 – Maximum Lateral Movement

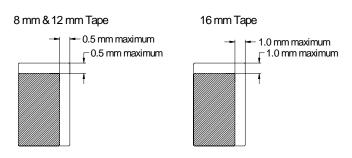


Figure 5 – Bending Radius

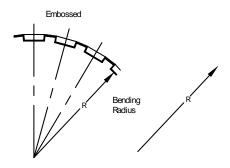




Figure 6 – Reel Dimensions

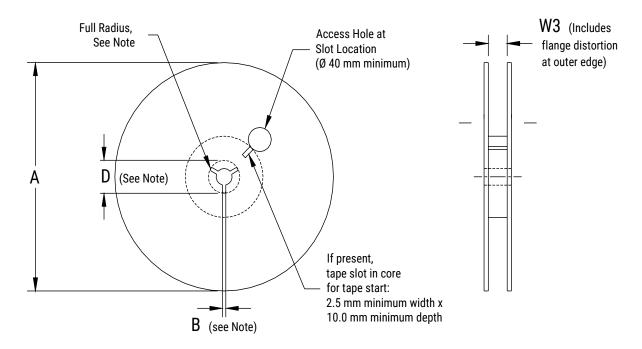




Figure 7 – Tape ader & Trailer Dimensions

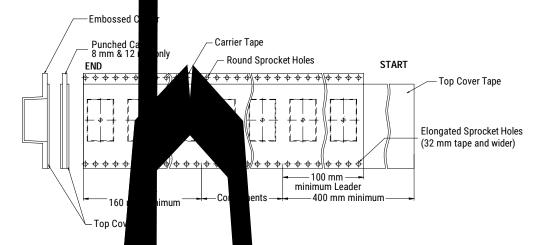
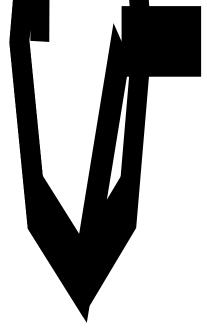


Figure 8 – Ma ım Camber





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