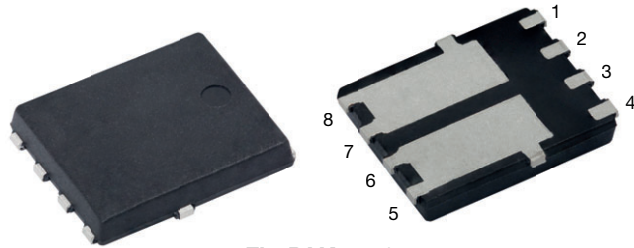
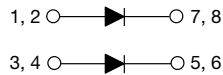


## Hyper Fast Rectifier, 2 x 3 A FRED Pt<sup>®</sup>


**FlatPAK 5 x 6**


### FEATURES

- Hyper fast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- Specific for output and snubber operation
- Low forward voltage drop
- Low leakage current
- AEC-Q101 qualified
- Meets MSL level 1 per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

 AUTOMOTIVE  
GRADE  
Available

**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### DESCRIPTION / APPLICATIONS

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### MECHANICAL DATA

Case: FlatPAK 5 x 6

Molding compound meets UL 94 V-0 flammability rating  
Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

PRODUCT SUMMARY	
Package	FlatPAK 5 x 6
$I_{F(AV)}$	2 x 3 A
$V_R$	200 V
$V_F$ at $I_F$	0.71 V
$t_{rr}$ (typ.)	25 ns
$T_J$ max.	175 °C
Diode variation	Separated cathode

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current per device	$I_{F(AV)}$	$T_{Solderpad} = 170\text{ °C, DC}$	6	A
		$T_{Solderpad} = 169\text{ °C, } D = 0.5$		
Non-repetitive peak surge current per device per diode	$I_{FSM}$	$T_J = 25\text{ °C, } 10\text{ ms sinusoidal pulse}$	173	
			87	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-55 to +175	°C



ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	200	-	-	V
Forward voltage, per diode	$V_F$	$I_F = 3\text{ A}$	-	0.88	0.94	
		$I_F = 3\text{ A}, T_J = 150\text{ }^\circ\text{C}$	-	0.71	0.74	
Reverse leakage current, per diode	$I_R$	$V_R = V_R$ rated	-	-	2	$\mu\text{A}$
		$T_J = 150\text{ }^\circ\text{C}, V_R = V_R$ rated	-	2	40	
Junction capacitance	$C_T$	$V_R = 200\text{ V}$	-	14	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1.0\text{ A}, di_F/dt = 50\text{ A}/\mu\text{s}, V_R = 30\text{ V}$	-	20	-	ns
		$I_F = 0.5\text{ A}, I_R = 1\text{ A}, I_{rr} = 0.25\text{ A}$	-	-	25	
		$T_J = 25\text{ }^\circ\text{C}$	-	15	-	
		$T_J = 125\text{ }^\circ\text{C}$	-	25	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^\circ\text{C}$	-	2	-	A
		$T_J = 125\text{ }^\circ\text{C}$	-	3	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^\circ\text{C}$	-	12	-	nC
		$T_J = 125\text{ }^\circ\text{C}$	-	40	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-55	-	175	$^\circ\text{C}$
Thermal resistance, junction to ambient, per diode	$R_{thJA}^{(1)(2)}$		-	90	103	$^\circ\text{C}/\text{W}$
Thermal resistance, junction to case, per diode	$R_{thJC}^{(3)}$		-	2.3	2.6	

**Notes**

- (1) The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{thJA}$   
(2) Free air, mounted or recommended copper pad area; thermal resistance  $R_{thJA}$  - junction to ambient  
(3) Mounted on infinite heatsink

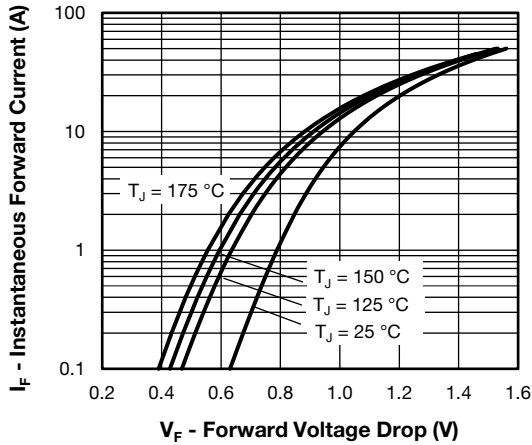


Fig. 1 - Typical Forward Voltage Drop Characteristics

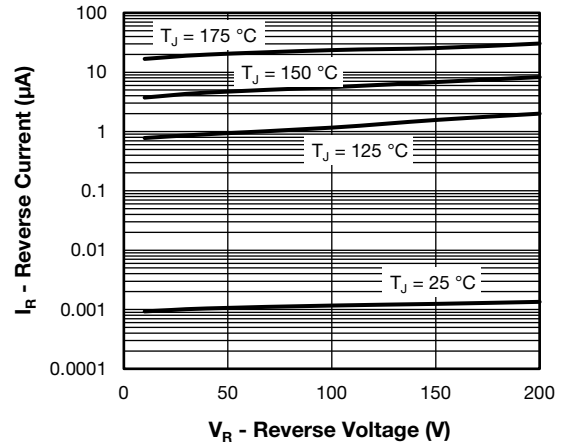


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

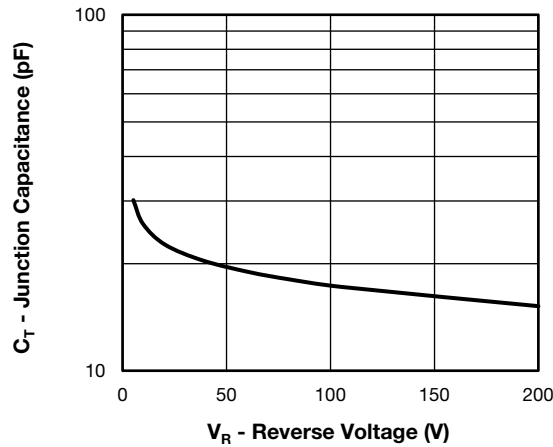


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

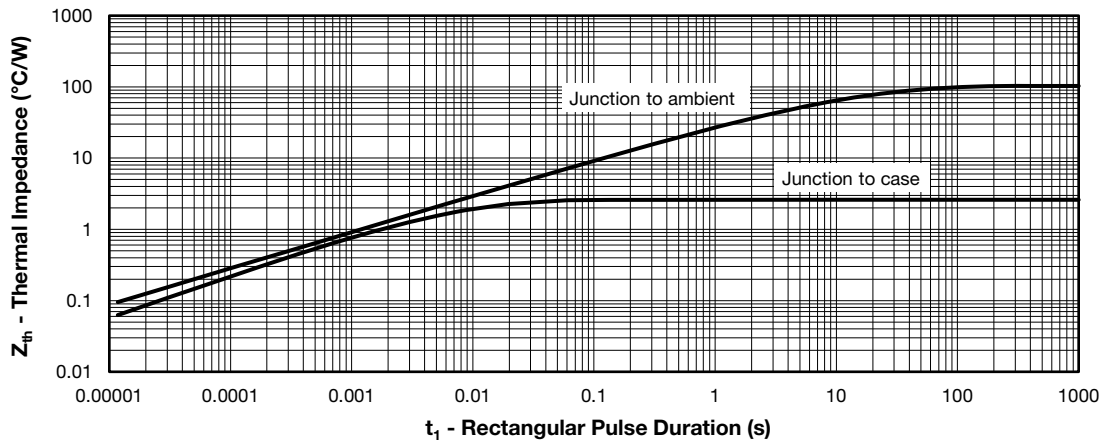


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

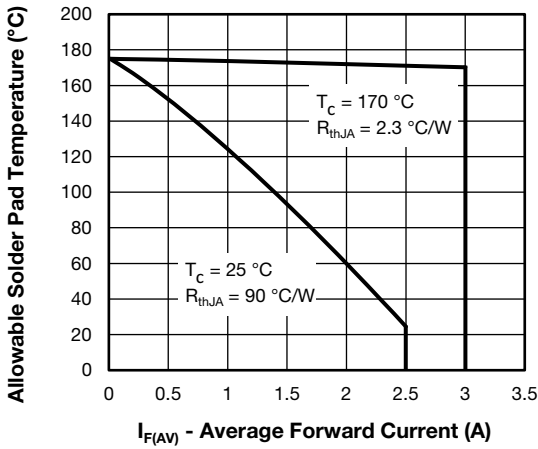


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

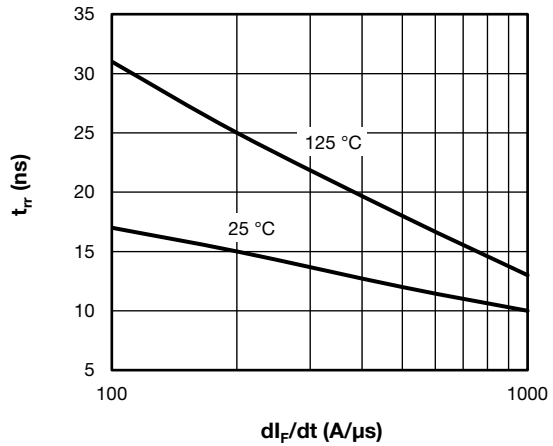


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

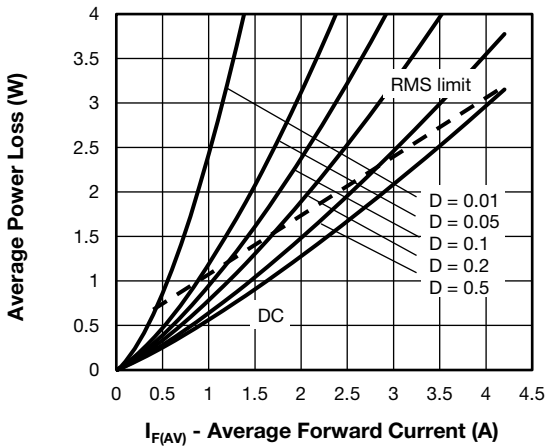


Fig. 6 - Forward Power Loss Characteristics

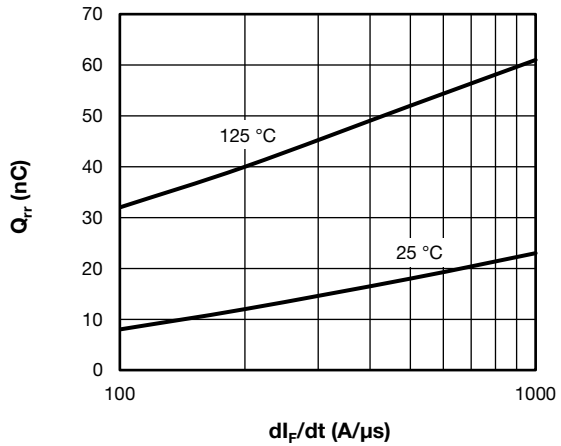
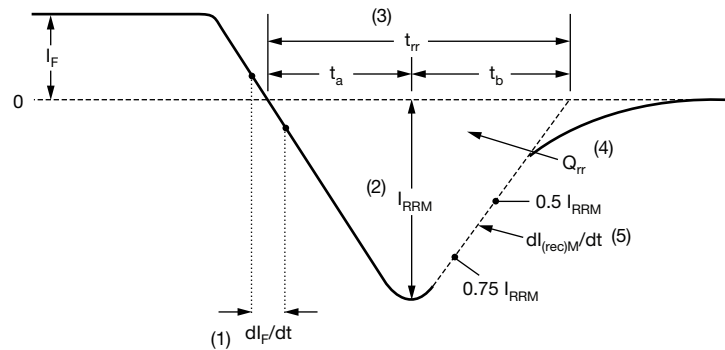


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$



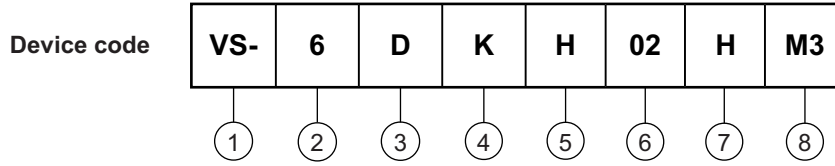
- (1)  $di_F/dt$  - rate of change of current through zero crossing
- (2)  $I_{RRM}$  - peak reverse recovery current
- (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- (5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

Fig. 9 - Reverse Recovery Waveform and Definitions



**ORDERING INFORMATION TABLE**



- 1** - Vishay Semiconductors product
- 2** - Current rating (6 = 6 A)
- 3** - Circuit configuration:  
D = separated cathode
- 4** - K = FlatPAK package
- 5** - Process type,  
H = hyperfast recovery
- 6** - Voltage code (02 = 200 V)
- 7** - H = AEC-Q101 qualified
- 8** - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

<b>ORDERING INFORMATION</b> (example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION
VS-6DKH02HM3/H	0.10	H	1500	7" diameter plastic tape and reel
VS-6DKH02-M3/I	0.10	I	6000	13" diameter plastic tape and reel

<b>LINKS TO RELATED DOCUMENTS</b>	
Dimensions	<a href="http://www.vishay.com/doc?96056">www.vishay.com/doc?96056</a>
Part marking information	<a href="http://www.vishay.com/doc?96059">www.vishay.com/doc?96059</a>
Packaging information	<a href="http://www.vishay.com/doc?88869">www.vishay.com/doc?88869</a>



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