



## Advanced Logic Design Trainer

### Specifications

Model	PB-502
<b>Input power Source</b>	Regulated wall mount adapter with outputs of +/-12VDC @ 200mA, 5VDC @ 250mA
<b>Selectable Operating Voltage</b>	Select either 5V TTL or 12V CMOS operation via jumper wire connection
<b>Power Supplies</b>	Fixed DC: 12VDC @ 200mA +/- 5% Fixed DC: (-)12VDC @ 200mA +/- 5% Fixed DC: 5VDC @ 250mA +/- 5%
<b>Clock</b>	Frequency: Fixed at three levels: 1hz +/- 20%, 1Khz +/- 20%, 100khz +/- 20% User variable with use of external capacitor
<b>Pulsers</b>	(2) Fully debounced pushbuttons with logic true and complementary outputs Logic '1' output current 2mA @ 4V min (5V TTL) Logic '1' output current 4mA @ 11V min (12V CMOS) Logic '0' output current 2mA max @ 0.1V max (5V/TTL) Logic '0' output current 5mA max @ 0.1V max (12V/CMOS)
<b>Logic Switches</b>	(8) SPDT switches select output of Vcc or ground (0V). Output current is 200mA max in all cases
<b>Logic Indicators</b>	(8) LEDs buffered by two 4-bit latches with separate enables. Input impedance 100k ohms in all cases
<b>Connectors</b>	(2) BNC – uncommitted
<b>Displays</b>	(2) BCD-input seven segment displays with separate Display Enable (DE). Latch Enable (LE), and Lamp Test (LT) inputs. Input impedance 100K ohms
<b>Breadboards</b>	840 tie points with (8) 25 pin power rails and accommodates up to 8 (14 pin) ICs
<b>Weight</b>	1 lb, 6oz (Minus Adapter) (0.5 kg)
<b>Dimensions</b>	10 x 7.5 x 2.6" (254 x 190 x 66 mm)

Technical data subject to change without notice.

### Optional Accessories

- WK-1:** Jumper Wire Kit, 350 pieces  
**WK-2:** Jumper Wire Kit, 140 pieces  
**WK-3:** Jumper Wire Kit, 70 pieces  
**WK-4:** Wire Jumper Kit, 100 wires with machined tips  
**GSPA Series:** Prototyping Adapters  
**GSPA-K1:** Surface mount to DIP Adapter Kit, 6 adapter boards  
**GSPA-K2:** Surface mount to DIP Adapter Kit, 11 adapter boards  
**GSA-3185:** Minipro Test Clip Set



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