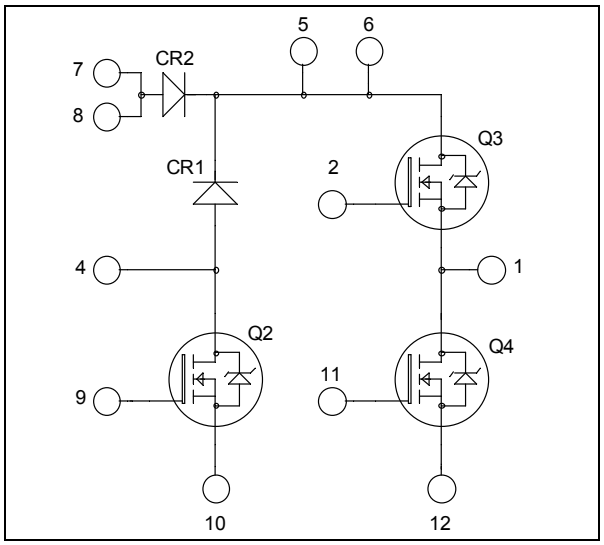
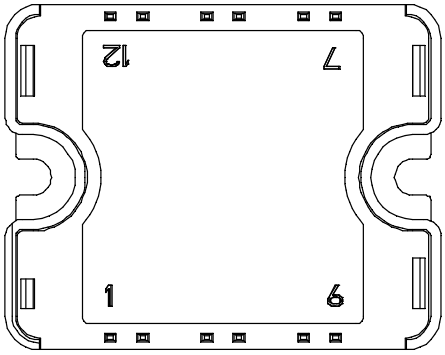
APTC60AM45B1G

Boost chopper & Phase Leg Super Junction MOSFET Power Module

V_{DSS} = 600V
R_{DSon} = 45mΩ max @ Tj = 25°C
I_D = 49A @ Tc = 25°C





Pins 7/8 ; 5/6 must be shorted together

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Solar converter

Features

- ***CoolMOS™***
  - Ultra low R_{DSon}
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
  - Very rugged
- ***By pass FRED diode (CR2)***

Benefits

- Very low stray inductance
- High level of integration
- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

All ratings @ T_j = 25°C unless otherwise specified

1. Phase leg (Q3 & Q4)

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{DSS}	Drain - Source Breakdown Voltage		600	V
I_D	Continuous Drain Current	T_c = 25°C	49	A
		T_c = 80°C	38	
I_{DM}	Pulsed Drain current		130	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		45	mΩ
P_D	Maximum Power Dissipation	T_c = 25°C	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		15	A
E_{AR}	Repetitive Avalanche Energy		3	mJ
E_{AS}	Single Pulse Avalanche Energy		1900	

Electrical Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0V, V_{DS} = 600V	T_j = 25°C			250	µA
		V_{GS} = 0V, V_{DS} = 600V	T_j = 125°C			500	
$R_{DS(on)}$	Drain – Source on Resistance	V_{GS} = 10V, I_D = 24.5A			40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 3mA		2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0V				100	nA

Dynamic Characteristics

Symbol	*Characteristic*	*Test Conditions*	*Min*	*Typ*	*Max*	*Unit*
C_{iss}	Input Capacitance	V_{GS} = 0V ; V_{DS} = 25V f = 1MHz		7.2		nF
C_{oss}	Output Capacitance			8.5		
Q_g	Total gate Charge	V_{GS} = 10V V_{Bus} = 300V I_D = 49A		150		nC
Q_{gs}	Gate – Source Charge			34		
Q_{gd}	Gate – Drain Charge			51		
$T_{d(on)}$	Turn-on Delay Time	**Inductive Switching (125°C)** V_{GS} = 10V V_{Bus} = 400V I_D = 49A R_G = 5Ω		21		ns
T_r	Rise Time			30		
$T_{d(off)}$	Turn-off Delay Time			100		
T_f	Fall Time			45		
R_{thJC}	Junction to Case Thermal Resistance				0.5	°C/W

Source - Drain diode ratings and characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_S	Continuous Source current (Body diode)		Tc = 25°C		49		A
			Tc = 80°C		38		
V_{SD}	Diode Forward Voltage	V_{GS} = 0V, I_S = - 49A				1.2	V
dv/dt	Peak Diode Recovery ❶					4	V/ns
t_{rr}	Reverse Recovery Time	I_S = - 49A V_R = 350V di_S/dt = 100A/µs	T_j = 25°C		600		ns
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		17		µC

❶ dv/dt numbers reflect the limitations of the circuit rather than the device itself.
$I_S \le$ - 49A di/dt ≤ 100A/µs $V_R \le V_{DSS}$ $T_j \le$ 150°C

2. Boost chopper (CR1 & Q2)

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{DSS}	Drain - Source Breakdown Voltage		600	V
I_D	Continuous Drain Current	T_c = 25°C	49	A
		T_c = 80°C	38	
I_{DM}	Pulsed Drain current		130	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		45	mΩ
P_D	Maximum Power Dissipation	T_c = 25°C	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		15	A
E_{AR}	Repetitive Avalanche Energy		3	mJ
E_{AS}	Single Pulse Avalanche Energy		1900	

Electrical Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0V, V_{DS} = 600V	T_j = 25°C			250	µA
		V_{GS} = 0V, V_{DS} = 600V	T_j = 125°C			500	
$R_{DS(on)}$	Drain – Source on Resistance	V_{GS} = 10V, I_D = 24.5A			40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 3mA		2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0V				100	nA

Dynamic Characteristics

Symbol	*Characteristic*	*Test Conditions*	*Min*	*Typ*	*Max*	*Unit*
C_{iss}	Input Capacitance	V_{GS} = 0V ; V_{DS} = 25V f = 1MHz		7.2		nF
C_{oss}	Output Capacitance			8.5		
Q_g	Total gate Charge	V_{GS} = 10V V_{Bus} = 300V I_D = 49A		150		nC
Q_{gs}	Gate – Source Charge			34		
Q_{gd}	Gate – Drain Charge			51		
$T_{d(on)}$	Turn-on Delay Time	**Inductive Switching (125°C)** V_{GS} = 10V V_{Bus} = 400V I_D = 49A R_G = 5Ω		21		ns
T_r	Rise Time			30		
$T_{d(off)}$	Turn-off Delay Time			100		
T_f	Fall Time			45		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 25°C** V_{GS} = 10V ; V_{Bus} = 400V I_D = 49A ; R_G = 5Ω		675		µJ
E_{off}	Turn-off Switching Energy			520		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 125°C** V_{GS} = 10V ; V_{Bus} = 400V I_D = 49A ; R_G = 5Ω		1100		µJ
E_{off}	Turn-off Switching Energy			635		
R_{thJC}	Junction to Case Thermal Resistance				0.5	°C/W

Diode ratings and characteristics (CR1)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V_R=600V	T_j = 25°C			25	µA
			T_j = 125°C			500	
I_F	DC Forward Current		Tc = 80°C		60		A
V_F	Diode Forward Voltage	I_F = 60A			1.7	2.3	V
		I_F = 120A			2		
		I_F = 60A	T_j = 125°C		1.4		
t_{rr}	Reverse Recovery Time	I_F = 600A V_R = 400V di/dt =200A/µs	T_j = 25°C		70		ns
			T_j = 125°C		140		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		100		nC
			T_j = 125°C		690		
R_{thJC}	Junction to Case Thermal Resistance					0.85	°C/W

3. By pass FRED diode (CR2)

Diode ratings and characteristics

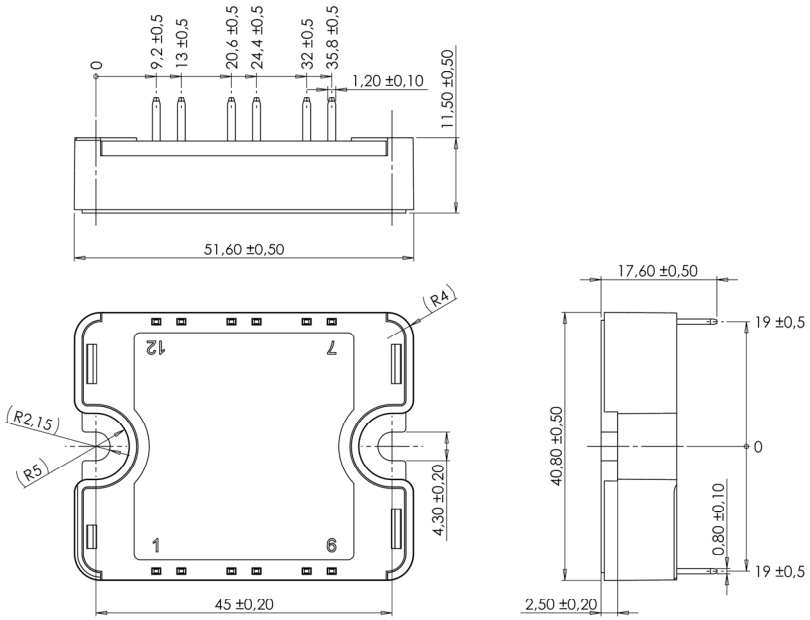
Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V_R=600V	T_j = 25°C			100	µA
			T_j = 150°C			350	
I_F	DC Forward Current		Tc = 80°C		30		A
V_F	Diode Forward Voltage	I_F = 30A V_{GE} = 0V	T_j = 25°C		1.6	2	V
			T_j = 150°C		1.5		
t_{rr}	Reverse Recovery Time	I_F = 30A V_R = 300V di/dt =1800A/µs	T_j = 25°C		100		ns
			T_j = 150°C		150		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		1.5		µC
			T_j = 150°C		3.1		
E_{rr}	Reverse Recovery Energy		T_j = 25°C		0.34		mJ
			T_j = 150°C		0.75		
R_{thJC}	Junction to Case Thermal Resistance					2.45	°C/W

4. Thermal & Package characteristics

Symbol	Characteristic			Min	Typ	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_J	Operating junction temperature range			-40		150*	°C
T_{STG}	Storage Temperature Range			-40		125	
T_C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					80	g

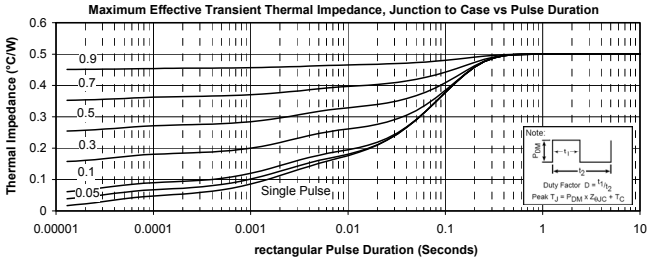
* T_{jmax} = 175°C for by pass diode

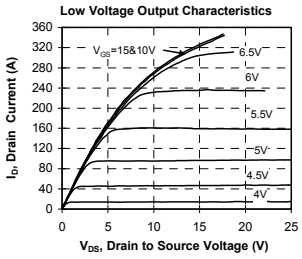
SP1 Package outline (dimensions in mm)

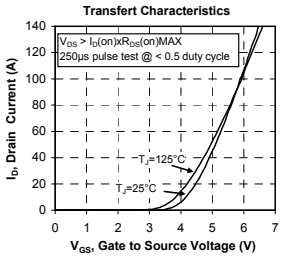


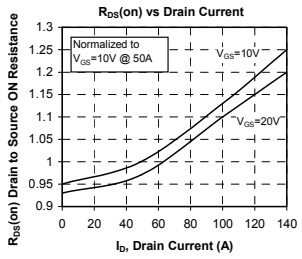
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

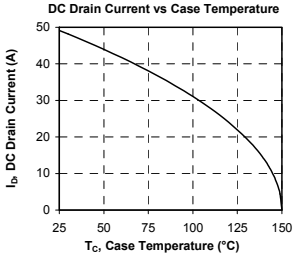
5. Typical CoolMOS Performance Curve (Phase leg)



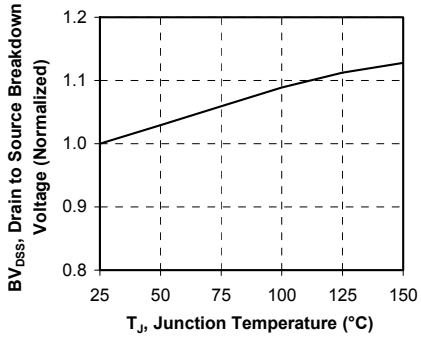




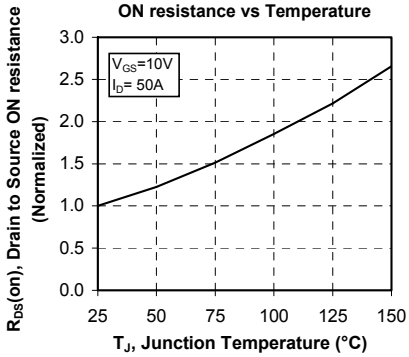




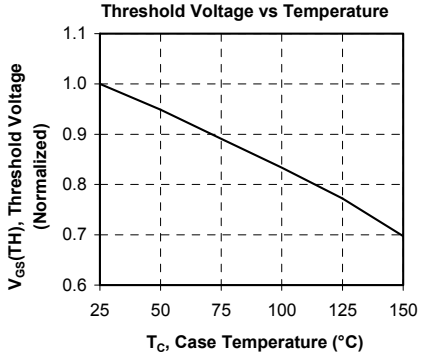
Breakdown Voltage vs Temperature



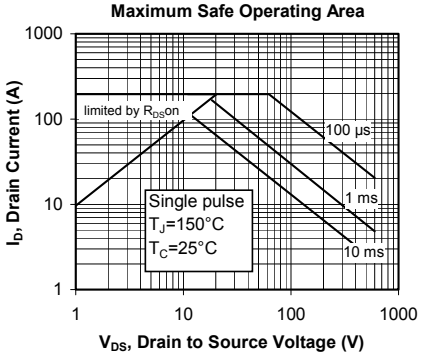
ON resistance vs Temperature



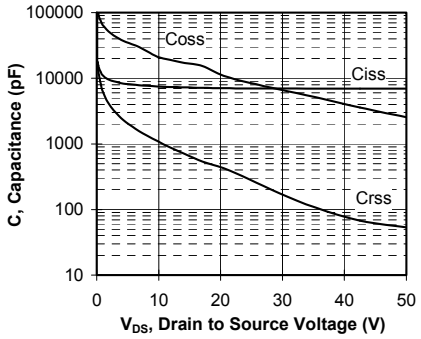
Threshold Voltage vs Temperature



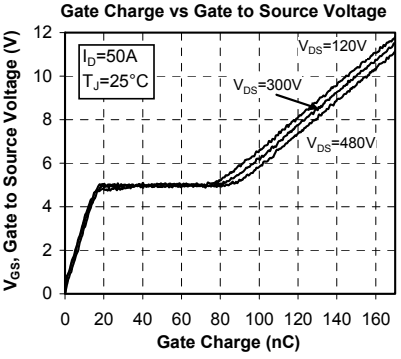
Maximum Safe Operating Area



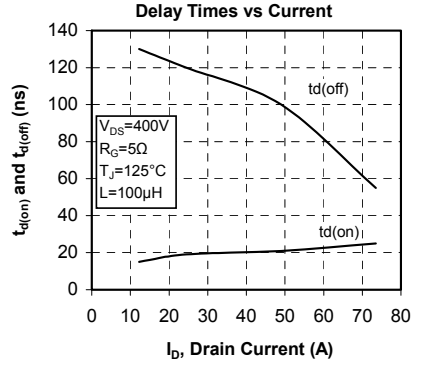
Capacitance vs Drain to Source Voltage



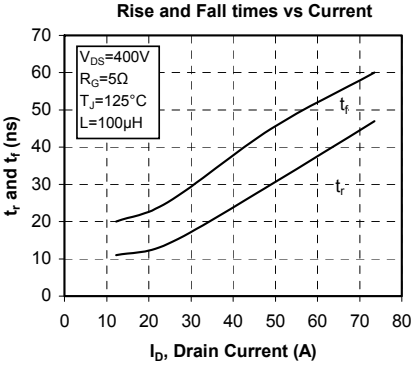
Gate Charge vs Gate to Source Voltage



Delay Times vs Current

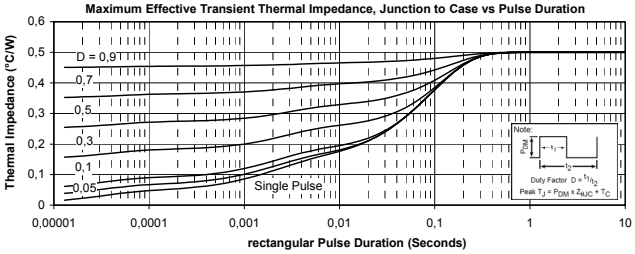


Rise and Fall times vs Current

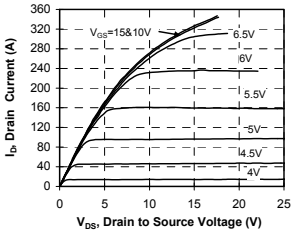


6. Typical CoolMOS Performance Curve (Boost chopper)

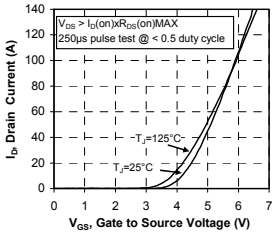
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



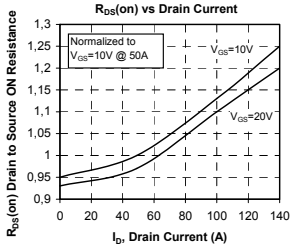
Low Voltage Output Characteristics



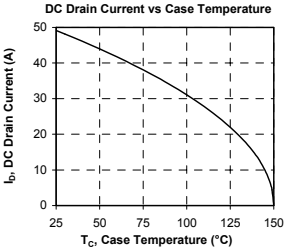
Transfert Characteristics



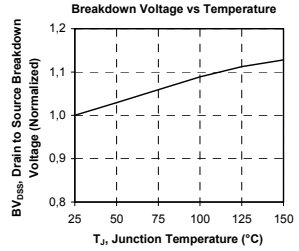
RDS(on) vs Drain Current



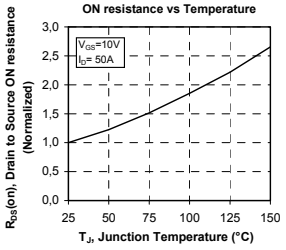
DC Drain Current vs Case Temperature



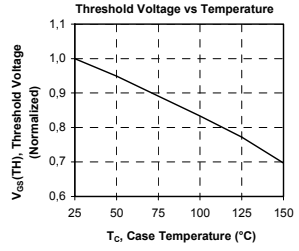
Breakdown Voltage vs Temperature



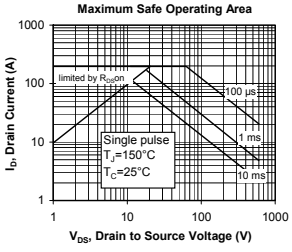
ON resistance vs Temperature



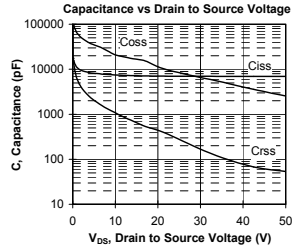
Threshold Voltage vs Temperature



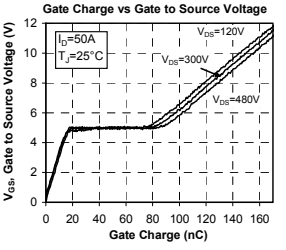
Maximum Safe Operating Area



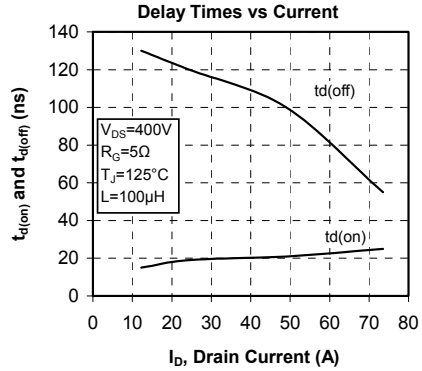
Capacitance vs Drain to Source Voltage



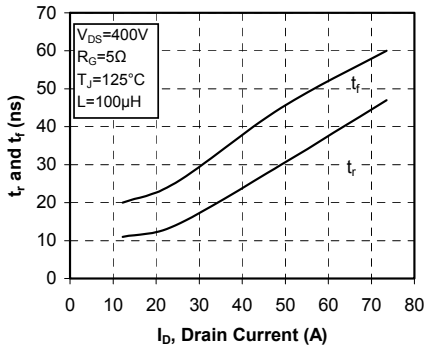
Gate Charge vs Gate to Source Voltage



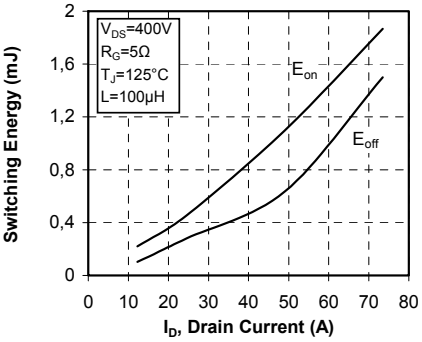
Delay Times vs Current



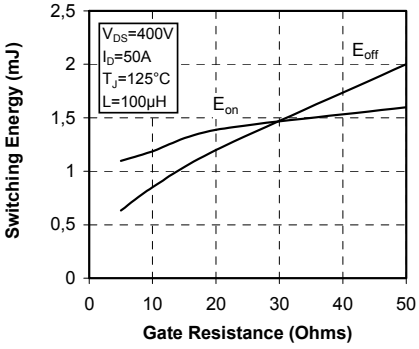
Rise and Fall times vs Current



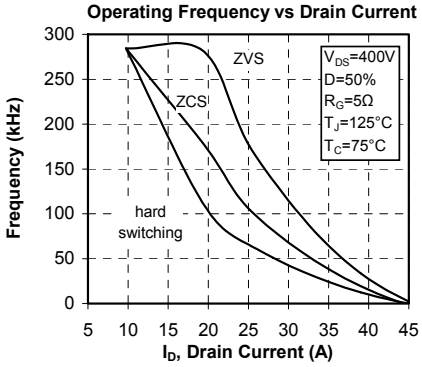
Switching Energy vs Current



Switching Energy vs Gate Resistance

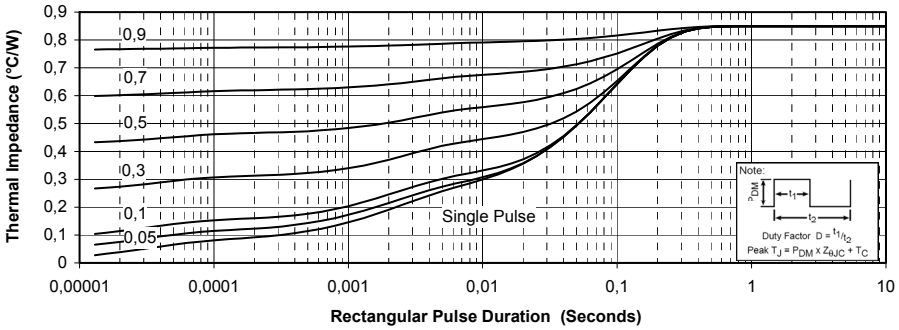


Operating Frequency vs Drain Current

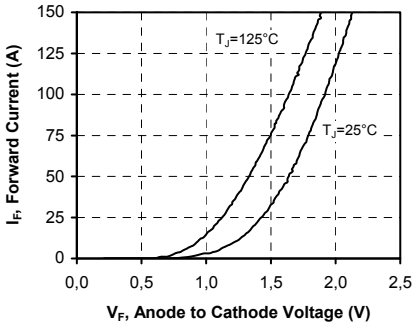


7. Typical Performance Curve (CR1)

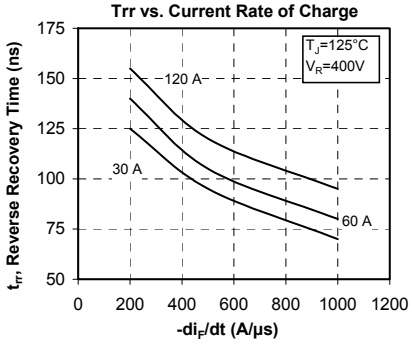
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



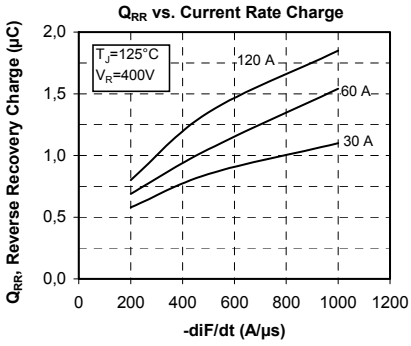
Forward Current vs Forward Voltage



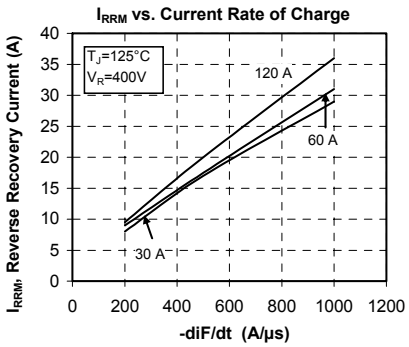
Trr vs. Current Rate of Charge



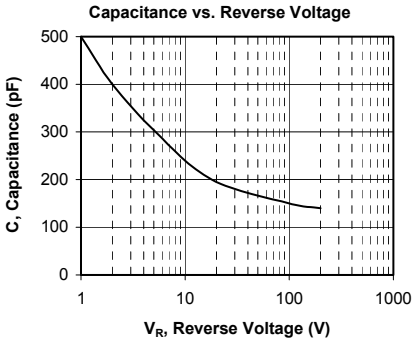
Q_{RR} vs. Current Rate Charge



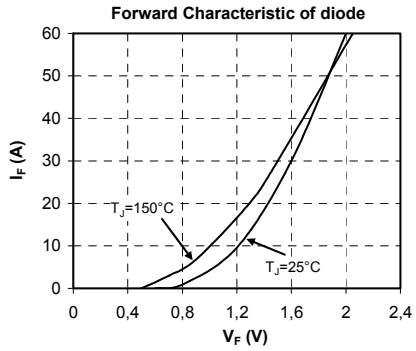
I_{RRM} vs. Current Rate of Charge

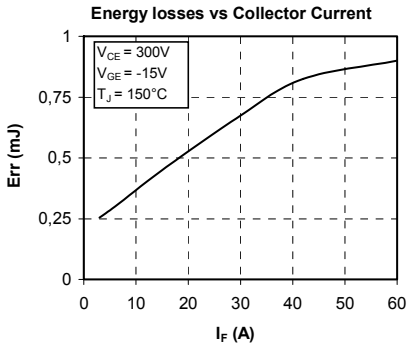


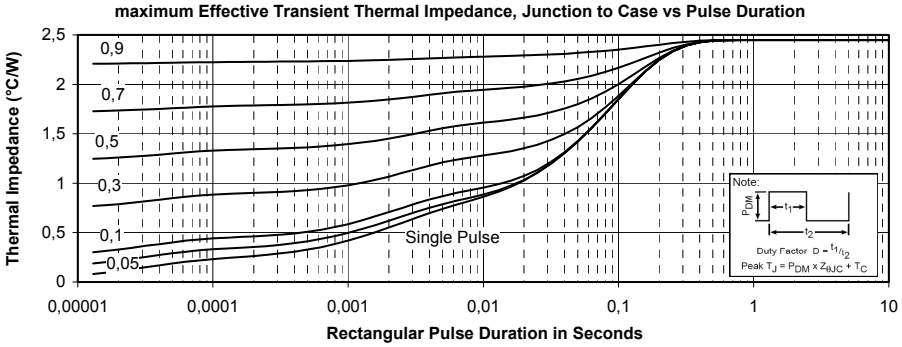
Capacitance vs. Reverse Voltage



8. Typical By pass Performance Curve (CR2)







"COOLMOS™ comprise a new family of transistors developed by Infineon Technologies AG. "COOLMOS" is a trademark of Infineon Technologies AG".

DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").
Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.